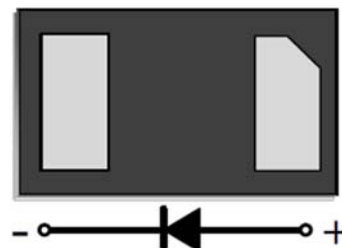


Features

- Low Forward Voltage
- Low Reverse Current
- Small package saves board space
- Compliant to Halogen-free
- Suffix "-Q1" for AEC-Q101

Applications

- Low current rectification
- Switch mode power supply
- Inverse polarity protection
- Low power consumption applications



Mechanical data

- **Package:** DFN-2L (0402)
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Polarity:** Cathode line denotes the cathode end
- **Marking Code:** FB3

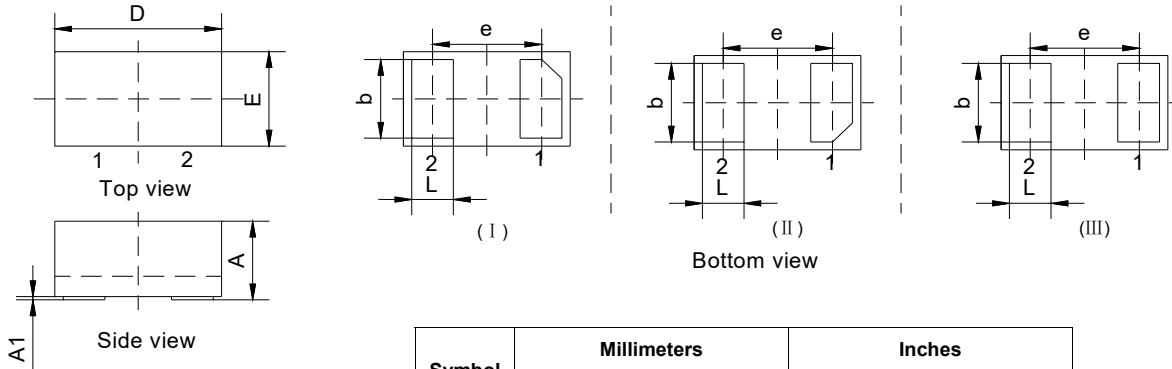
Maximum ratings (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Maximum repetitive peak reverse voltage	V_{RRM}	40	V
Maximum RMS voltage	V_{RMS}	28	V
Maximum DC blocking voltage	V_{DC}	40	V
Average forward current	$I_{F(AV)}$	0.2	A
Peak forward surge current: 1s single half sine-wave superimposed on rated load	I_{FSM}	1	A
Operating junction temperature	T_j	125	$^{\circ}\text{C}$
Storage temperature range	T_{stg}	-55 to +150	$^{\circ}\text{C}$

Electrical characteristics (AT $T_A=25^{\circ}\text{C}$ unless otherwise noted)

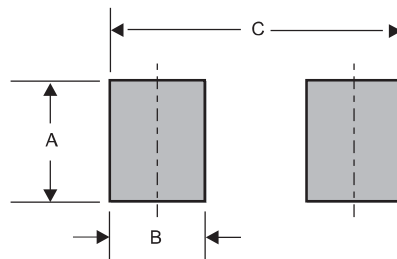
Parameter		Symbol	Min.	Typ.	Max.	Unit
Breakdown voltage	$I_R=100\mu\text{A}$	V_{BR}	40			V
Forward voltage	$I_F=10\text{mA}$	V_F			0.39	V
	$I_F=100\text{mA}$				0.55	V
Reverse leakage current	$V_R=10\text{V}$	I_R			1	μA
	$V_R=40\text{V}$				10	

Package Outline Dimensions



Symbol	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.40	0.50	0.55	0.016	0.020	0.022
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.45	0.50	0.55	0.018	0.020	0.022
D	0.95	1.00	1.05	0.037	0.039	0.041
e	0.65BSC			0.026BSC		
E	0.55	0.60	0.65	0.022	0.024	0.026
L	0.20	0.25	0.30	0.008	0.010	0.012

Suggested Land Pattern



Dimensions in millimeters

PACKAGE	A	B	C
DFN-2L	0.024(0.60)	0.014(0.35)	0.040(1.00)