

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
-60V	$3\Omega @ -10V$	-0.3A
	$3.5\Omega @ -4.5V$	

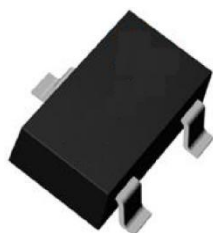
Feature

- Operated at Low Logic Level Gate Drive
- P-Channel Switch with Low $R_{DS(on)}$
- Suffix "-Q1" for AEC-Q101

Application

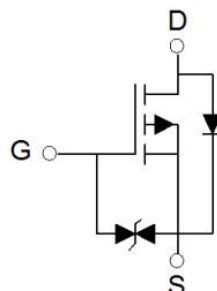
- power management
- Portable equipment

Package

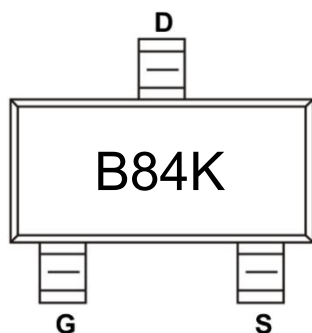


SOT-23

Circuit diagram



Marking



Absolute maximum ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	-60	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D	-0.3	A
Pulsed Drain Current ¹⁾	I _{DM}	-1.2	A
Power Dissipation ²⁾	P _D	0.5	W
Thermal Resistance Junction to Ambient	R _{θJA}	250	°C/W
Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Electrical characteristics (T_J=25 °C unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-60			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = -60V, V _{GS} = 0V			-1	μA
Gate-body leakage current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±10	μA
Gate threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.0	-1.5	-2.0	V
Drain-source on-resistance	R _{DS(on)}	V _{GS} = -10V, I _D = -0.3A		2.2	3	Ω
		V _{GS} = -4.5V, I _D = -0.1A		2.5	3.5	
Dynamic characteristics ³⁾						
Input Capacitance	C _{iss}	V _{DS} = -30V, V _{GS} = 0V, f = 1MHz		35		pF
Output Capacitance	C _{oss}			6		
Reverse Transfer Capacitance	C _{rss}			3		
Total Gate Charge	Q _g	V _{GS} = -10V, V _{DS} = -30V, I _D = -1A		1.7		nC
Gate-Source Charge	Q _{gs}			0.6		
Gate-Drain Charge	Q _{gd}			0.2		
Turn-on delay time	t _{d(on)}	V _{GS} = -10V, V _{DD} = -30V I _D = -1A, R _{GEN} = 2.3Ω		6		nS
Turn-on rise time	t _r			21		
Turn-off delay time	t _{d(off)}			31		
Turn-off fall time	t _f			32		
Source-Drain Diode characteristics						
Diode Forward Current	I _S				-0.3	V
Diode Forward voltage	V _{SD}	V _{GS} = 0V, I _S = -0.3A			-1.3	V

Notes:

- 1) Repetitive rating; pulse width limited by max. junction temperature.
- 2) P_D is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.
- 3) Guaranteed by design, not subject to production testing.

Typical Characteristics

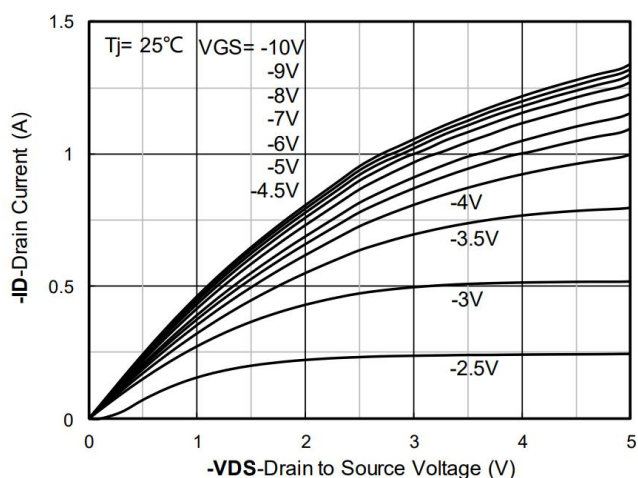


Figure 1. Output Characteristics

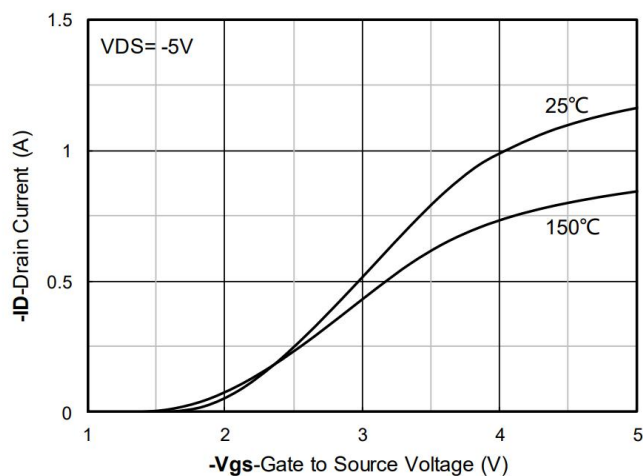


Figure 2. Transfer Characteristics

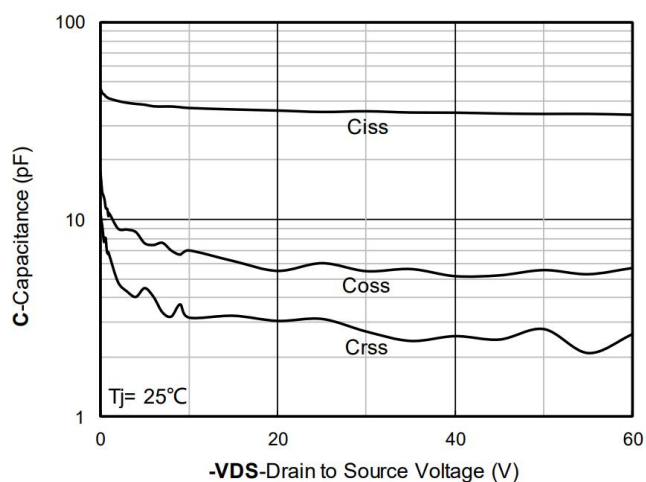


Figure 3. Capacitance Characteristics

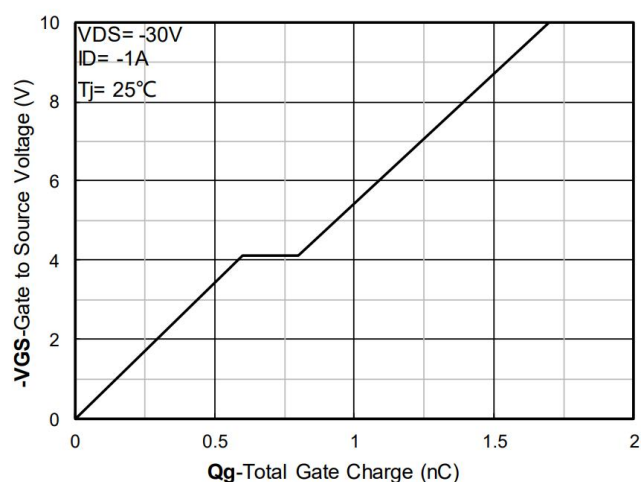


Figure 4. Gate Charge

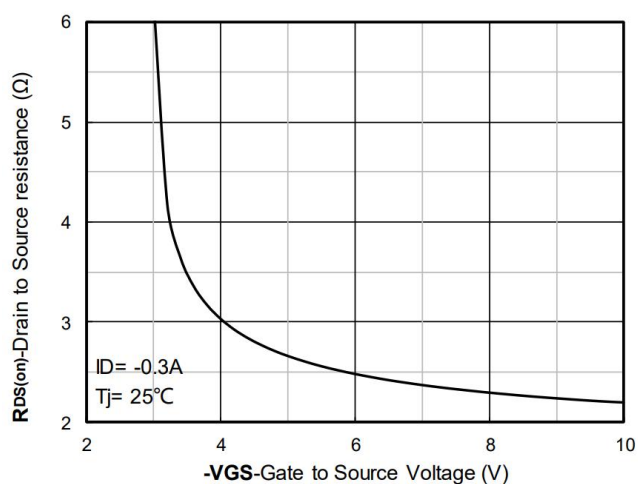


Figure 5. On-Resistance vs Gate to Source Voltage

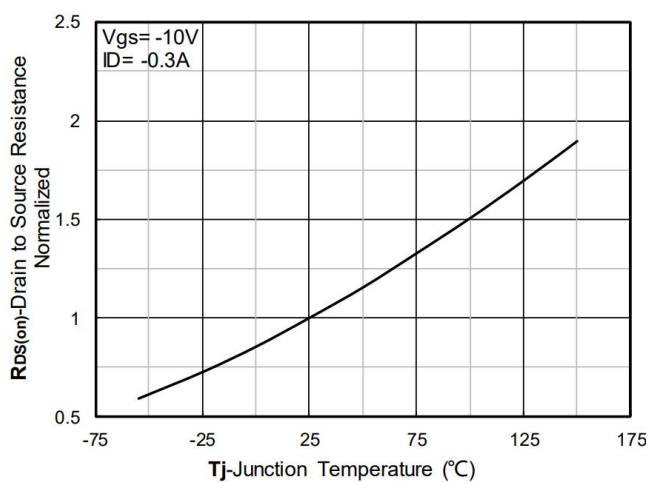


Figure 6. Normalized On-Resistance

Typical Characteristics

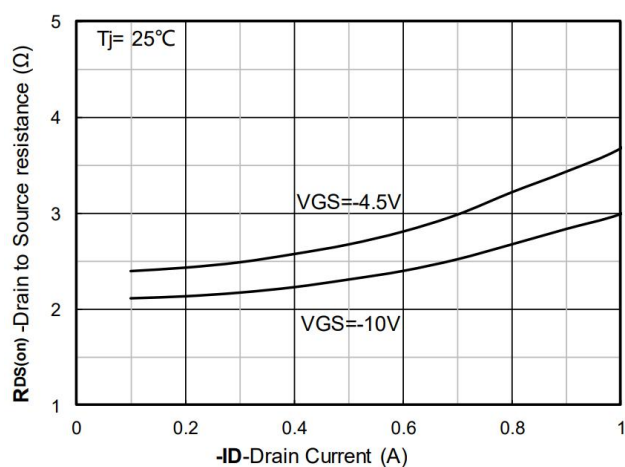


Figure 7. $R_{DS(on)}$ VS Drain Current

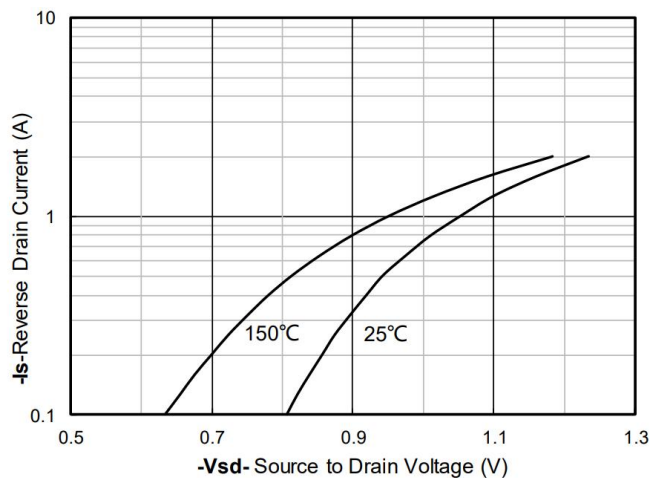


Figure 8. Forward characteristics of reverse diode

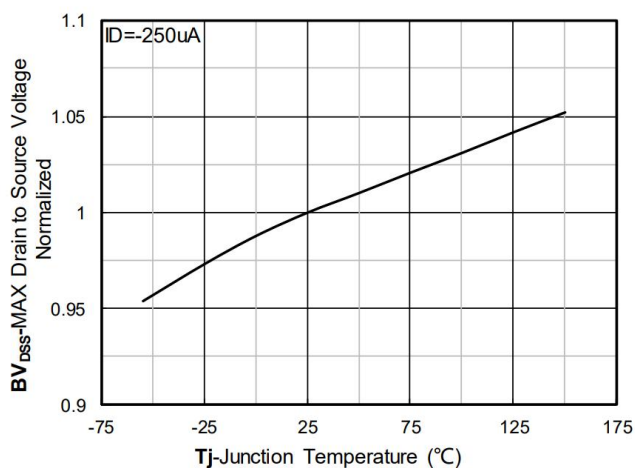


Figure 9. Normalized breakdown voltage

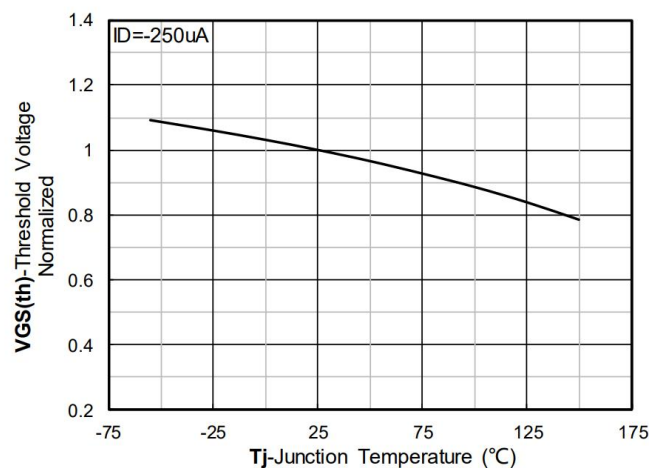


Figure 10. Normalized Threshold voltage

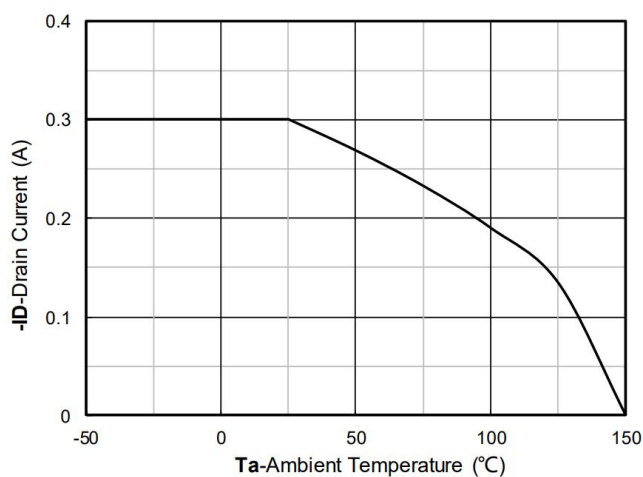


Figure 11. Current dissipation

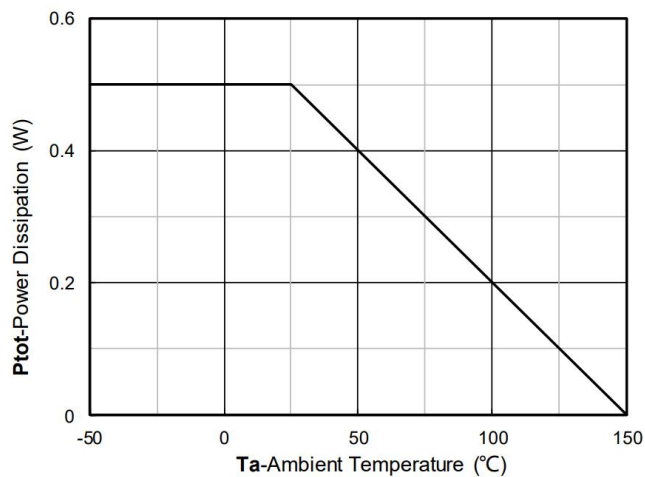


Figure 12. Power dissipation

Typical Characteristics

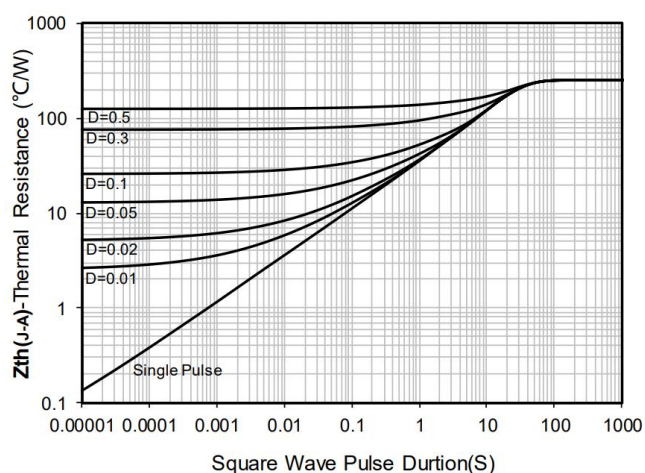


Figure 13. Maximum Transient Thermal Impedance

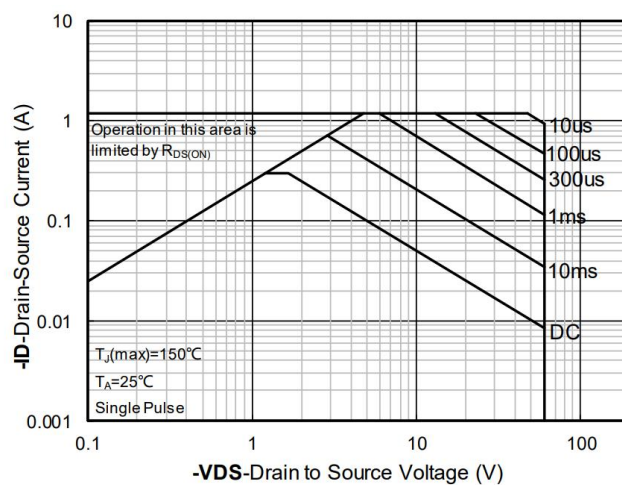
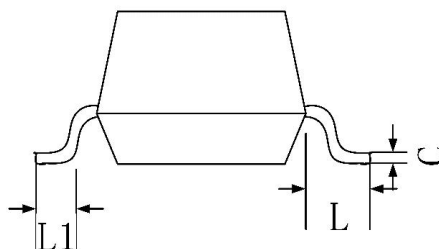
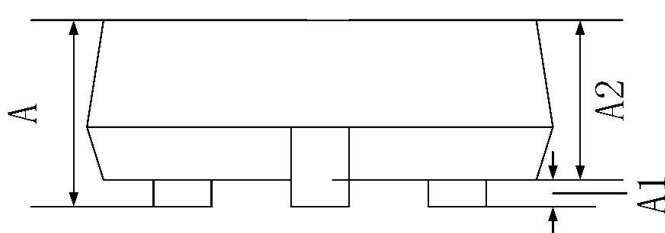
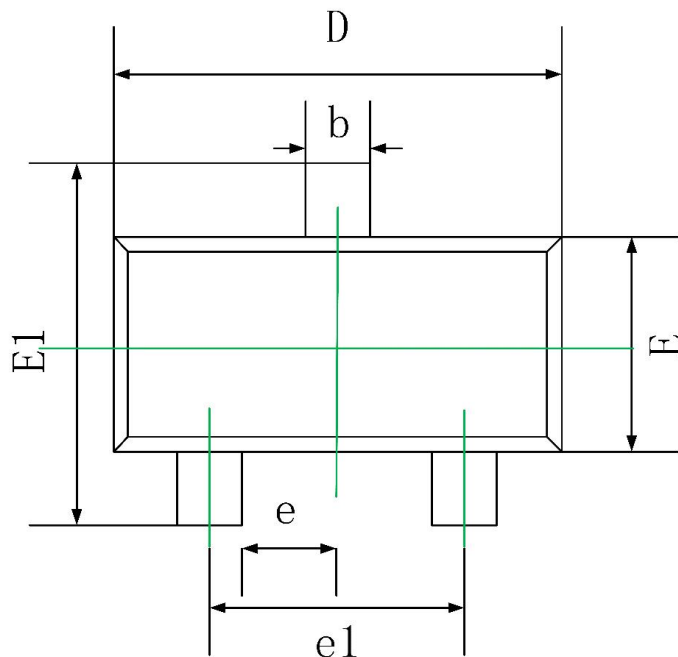


Figure 14. Safe Operation Area

SOT-23 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.200	0.003	0.008
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020