AT012SN100MQTA-Q1

N-Channel Enhancement Mode MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
100V	120mΩ@10V	12A
	135mΩ@4.5V	

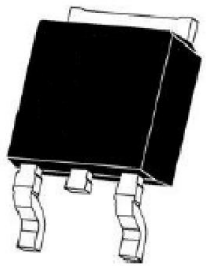
Feature

- Trench power MV MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$
- Suffix “-Q1” for AEC-Q101

Application

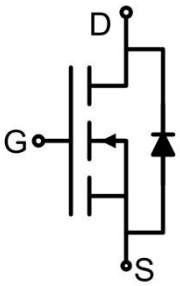
- Power management functions
- DC-DC convertor

Package

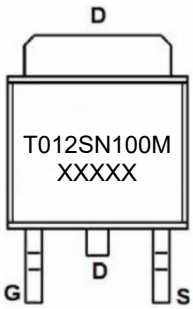


TO-252AB

Circuit diagram



Marking



Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current (T_C=25℃)	I_D	12	A
Continuous Drain Current (T_C=100℃)	I_D(100℃)	8.5	A
Pulsed Drain Current[1]	I_{DM}	25	A
Single Pulse Avalanche Energy[2]	E_{AS}	12	mJ
Power Dissipation[3] (T_C=25℃)	P_D	50	W
Thermal Resistance Junction to Case	$R_{\theta JC}$	3	℃/W
Operating Junction Temperature	T_J	-55 ~ +175	℃
Storage Temperature	T_{STG}	-55 ~ +175	℃

Electrical characteristics (T_J=25℃ unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS} =0V, I_D =250μA	100			V
Zero gate voltage drain current	I_{DSS}	V_{DS} =100V, V_{GS} =0V			1	μA
Gate-body leakage current	I_{GSS}	V_{DS} =0V, V_{GS} =±20V			±100	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =250μA	1.1	1.8	2.5	V
Drain-source on-resistance	$R_{DS(on)}$	V_{GS} =10V, I_D =10A		93	120	mΩ
		V_{GS} =4.5V, I_D =10A		97	135	
Dynamic characteristics[4]						
Input Capacitance	C_{iss}	V_{DS} =50V, V_{GS} =0V, f =1MHz		940		pF
Output Capacitance	C_{oss}			28		
Reverse Transfer Capacitance	C_{rss}			20		
Total Gate Charge	Q_g	V_{DS} =50V, V_{GS} =10V, I_D =12A		19		nC
Gate-Source Charge	Q_{gs}			3.1		
Gate-Drain Charge	Q_{gd}			3.4		
Turn-on delay time	$t_{d(on)}$	V_{DS} =50V, V_{GS} =10V, I_D =12A R_G =2.2Ω		7		nS
Turn-on rise time	t_r			25		
Turn-off delay time	$t_{d(off)}$			21		
Turn-off fall time	t_f			2.5		
Source-Drain Diode characteristics						
Diode Forward Current	I_S				12	A
Diode Forward voltage	V_{SD}	V_{GS} =0V, I_S =12A			1.2	V
Reverse Recovery Time	T_{rr}	I_F =12A, di/dt =-100A/μs		28		nS
Reverse Recovery Charge	Q_{rr}			27		nC

Notes:

1) Repetitive rating; pulse width limited by max. junction temperature.
2) T_J=25℃, V_G=10V, R_G=25Ω, L=0.5mH, I_{AS}=7A.
3) Pd is based on max. junction temperature, using junction-case thermal resistance.
4) Guaranteed by design, not subject to production testing.

Typical Characteristics

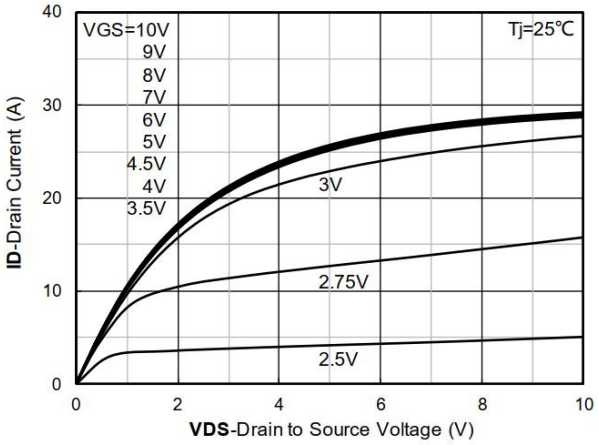


Figure 1. Output Characteristics

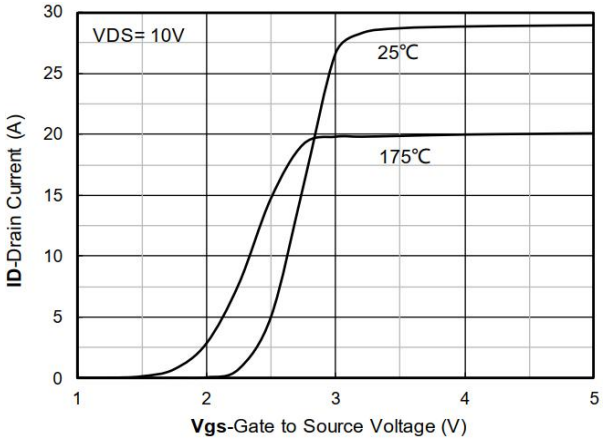


Figure 2. Transfer Characteristics

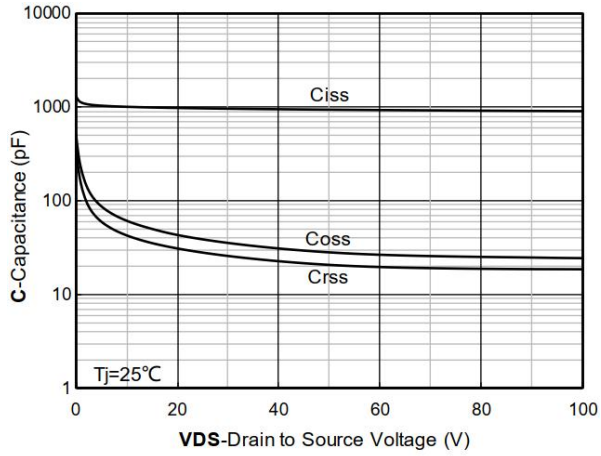


Figure 3. Capacitance Characteristics

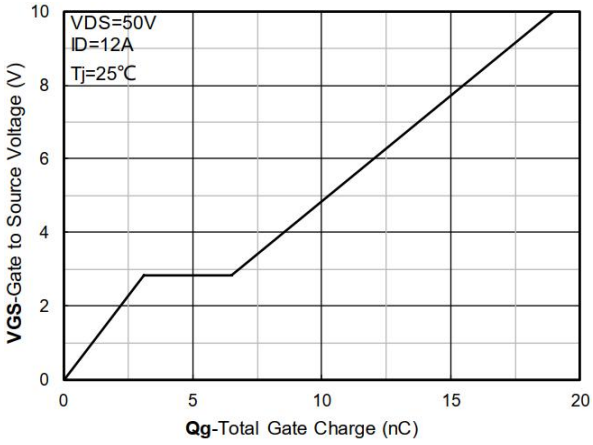


Figure 4. Gate Charge

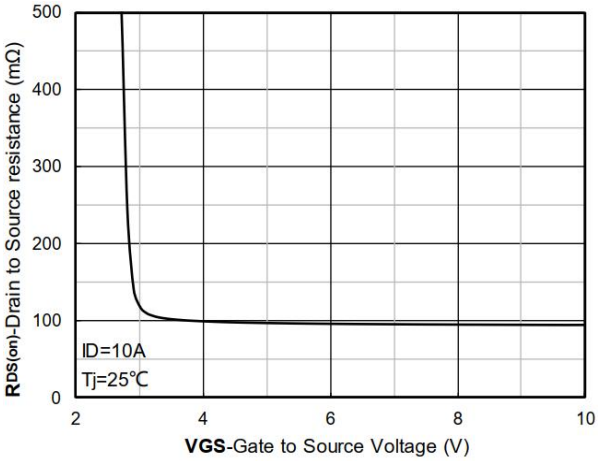


Figure 5. On-Resistance vs Gate to Source Voltage

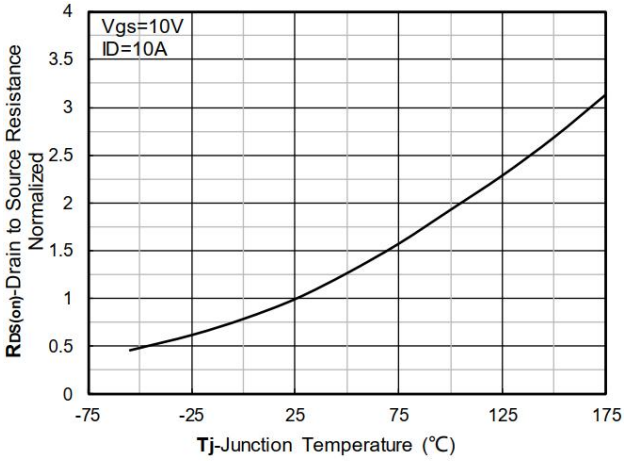


Figure 6. Normalized On-Resistance

Typical Characteristics

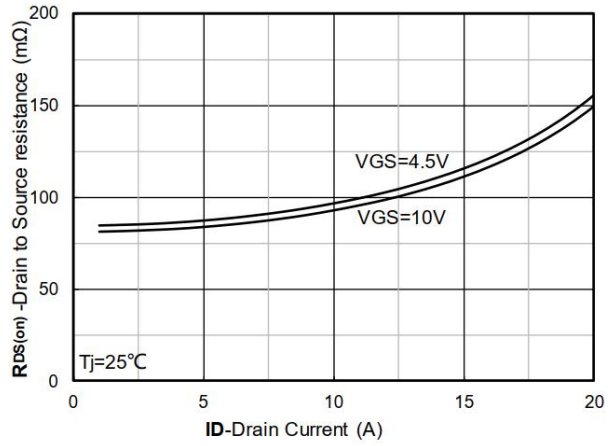


Figure 7. $R_{DS(on)}$ VS Drain Current

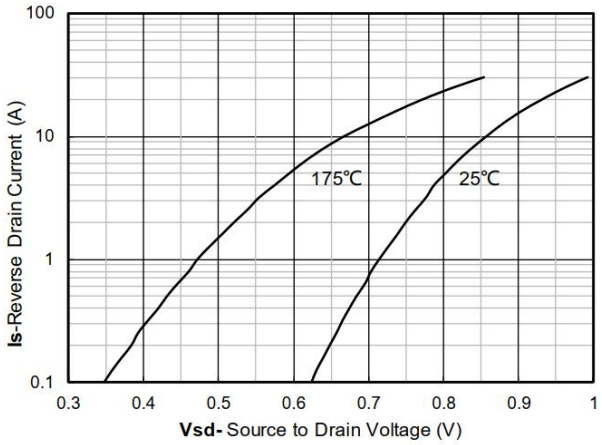


Figure 8. Forward characteristics of reverse diode

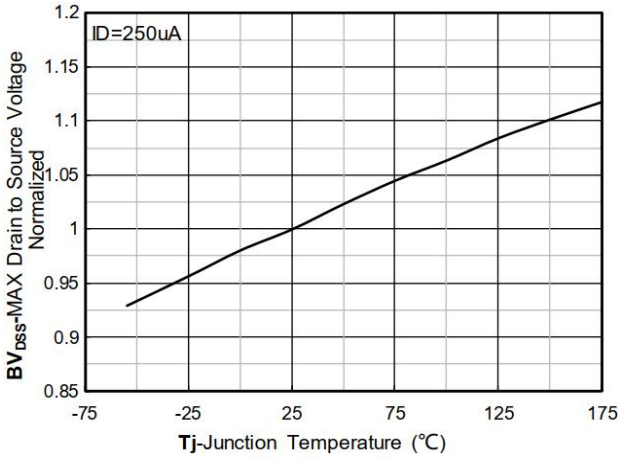


Figure 9. Normalized breakdown voltage

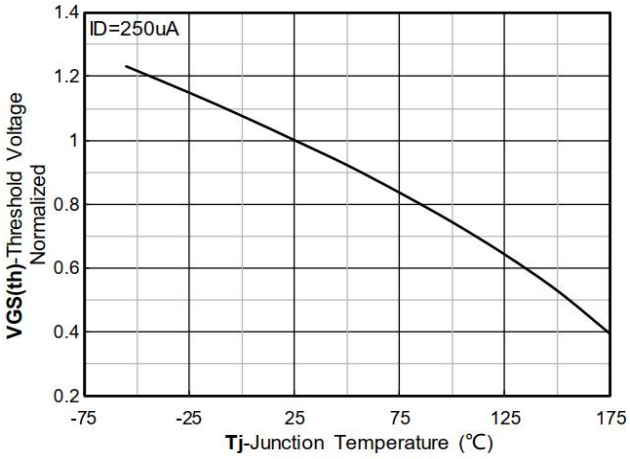


Figure 10. Normalized Threshold voltage

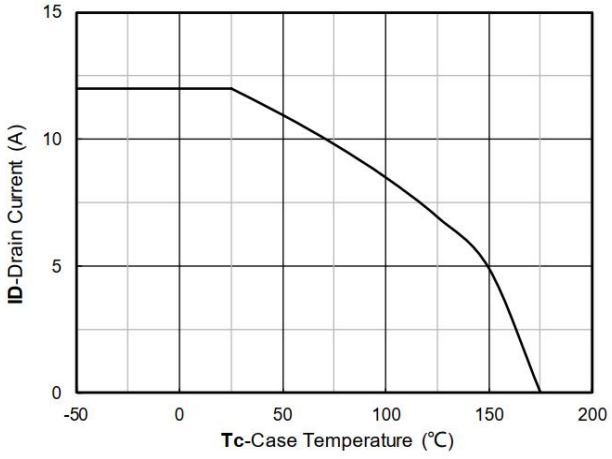


Figure 11. Current dissipation

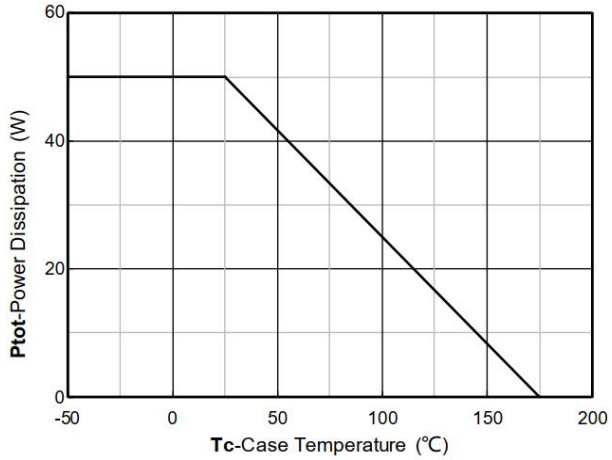


Figure 12. Power dissipation

Typical Characteristics

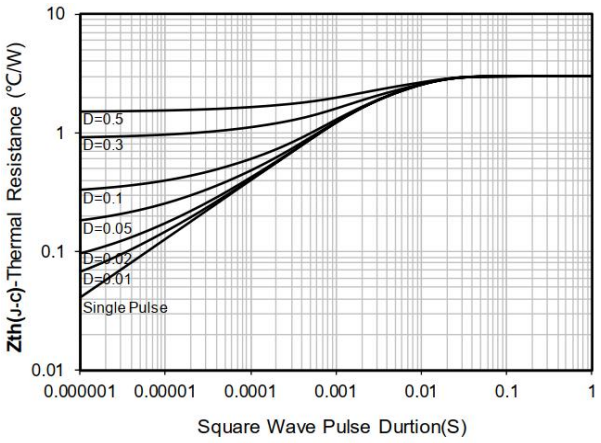


Figure 13. Maximum Transient Thermal Impedance

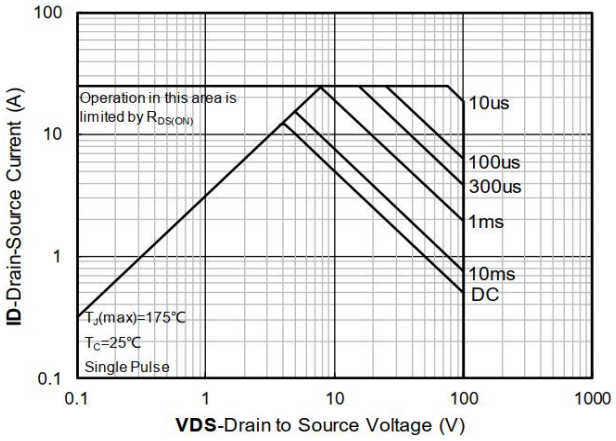
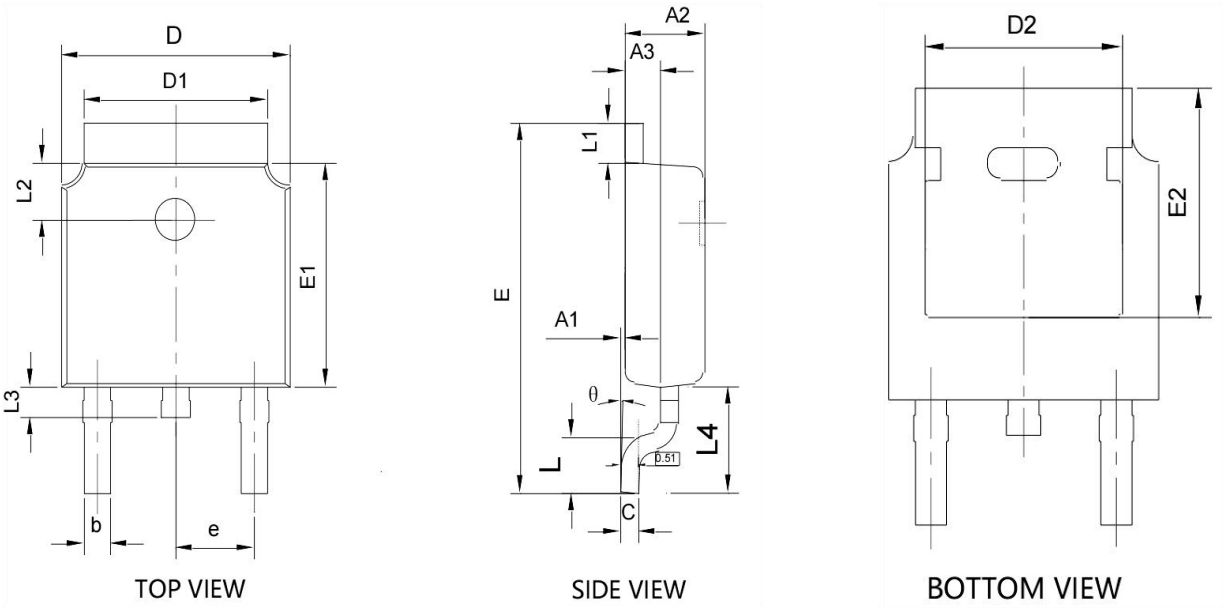


Figure 14. Safe Operation Area

TO-252AB Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A1	0.000	0.200	0.000	0.008
A2	2.200	2.400	0.087	0.094
A3	0.900	1.100	0.035	0.043
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.150	5.450	0.203	0.215
D2	4.600	4.950	0.181	0.195
E	9.900	10.300	0.390	0.406
E1	6.000	6.200	0.236	0.244
E2	5.150	5.450	0.203	0.215
e	2.286 BSC.		0.090 BSC.	
L	1.250	1.750	0.049	0.069
L1	0.900	1.270	0.035	0.050
L2	1.400	1.900	0.055	0.075
L3	0.600	1.000	0.024	0.039
L4	2.900 REF.		0.114 REF.	
θ	0°	10°	0°	10°