

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
-30V	19mΩ@-10V	-10A
	28mΩ@-4.5V	

Feature

- Trench Power LV MOSFET technology
- High density cell design for low $R_{DS(ON)}$
- High Speed switching

Application

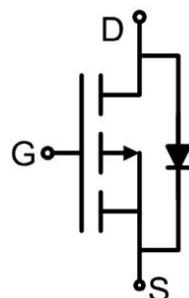
- Battery protection
- Load switch
- Power management

Package



SOP-8

Circuit diagram



Marking



Absolute maximum ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D	-10	A
Continuous Drain Current(T _A =100°C)	I _D (100°C)	-6.3	A
Pulsed Drain Current ¹⁾	I _{DM}	-80	A
Avalanche energy ²⁾	E _{AS}	56	mJ
Power Dissipation ³⁾	P _D	2.5	W
Thermal Resistance,Junction-to-Ambient ⁴⁾	R _{θJA}	50	°C/W
Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Electrical characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-30			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = -30V, V _{GS} = 0V			-1	μA
Gate-body leakage current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.0	-1.5	-2.5	V
Drain-source on-resistance	R _{DS(on)}	V _{GS} = -10V, I _D = -10A		14	19	mΩ
		V _{GS} = -4.5V, I _D = -5A		20	28	
Dynamic characteristics⁵⁾						
Input Capacitance	C _{iss}	V _{DS} = -15V, V _{GS} = 0V, f = 1MHz		1220		pF
Output Capacitance	C _{oss}			170		
Reverse Transfer Capacitance	C _{rss}			160		
Total Gate Charge	Q _g	V _{DS} = -15V, V _{GS} = -10V, I _D = -10A		24		nC
Gate-Source Charge	Q _{gs}			2		
Gate-Drain Charge	Q _{gd}			6		
Turn-on delay time	t _{d(on)}	V _{DD} = -15V, V _{GS} = -10V, I _D = -10A, R _{GEN} = 2.5Ω		11		nS
Turn-on rise time	t _r			4		
Turn-off delay time	t _{d(off)}			70		
Turn-off fall time	t _f			50		
Source-Drain Diode characteristics						
Diode Forward Current	I _S				-10	A
Diode Forward voltage	V _{SD}	V _{GS} = 0V, I _S = -10A			-1.2	V
Reverse Recovery Time	t _{rr}	I _F = -10A, di/dt = 100A/us		35		nS
Reverse Recovery Charge	Q _{rr}			11		nC

Notes:

- 1) Repetitive rating; pulse width limited by max. junction temperature.
- 2) T_J=25°C, V_{DD}=-30V, V_G=-10V, R_G=25Ω, L=0.5mH, I_{AS}=-15A.
- 3) P_d is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.
- 4) The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in the still air environment with T_A=25°C. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 5) Guaranteed by design, not subject to production.

Typical Characteristics

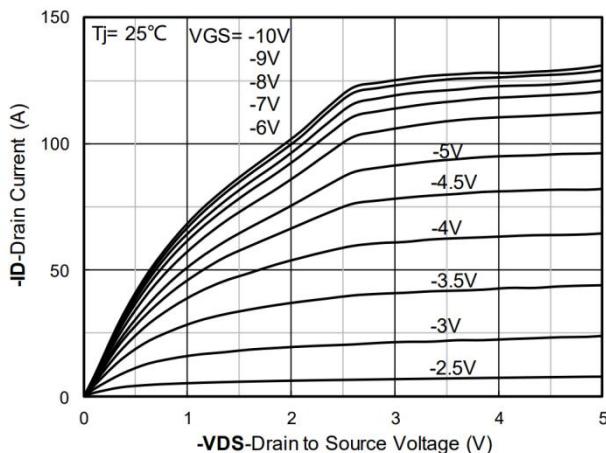


Figure 1. Output Characteristics

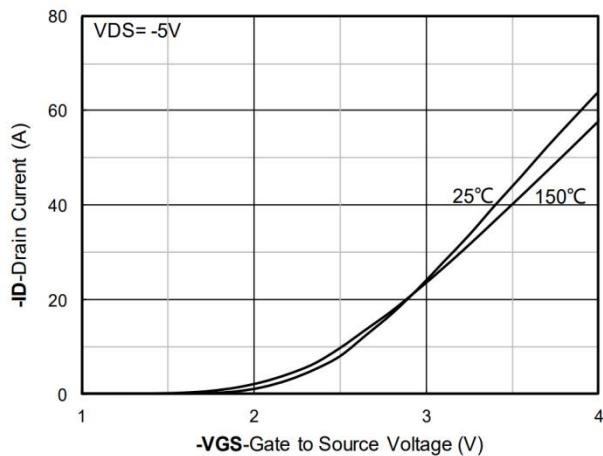


Figure 2. Transfer Characteristics

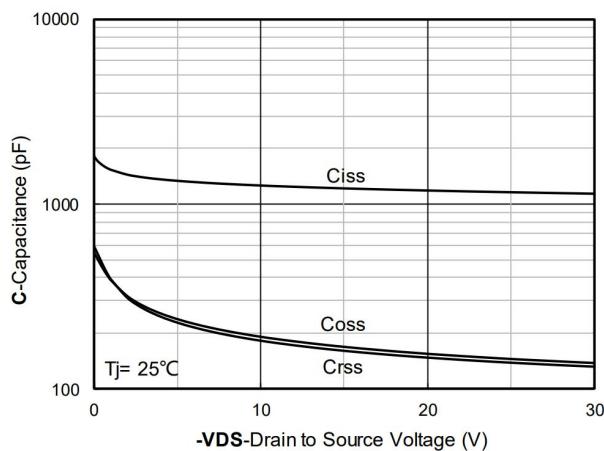


Figure 3. Capacitance Characteristics

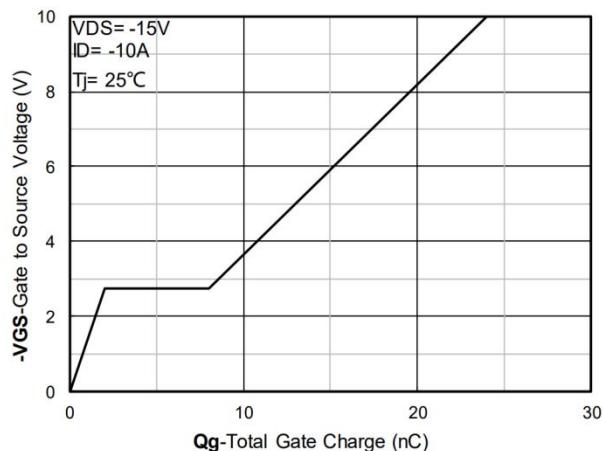


Figure 4. Gate Charge

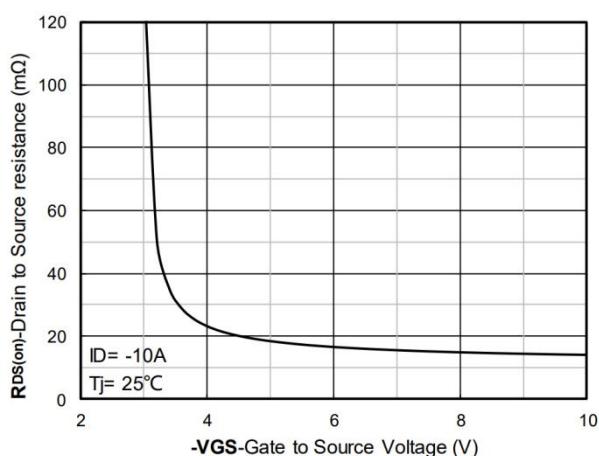


Figure 5. On-Resistance vs Gate to Source Voltage

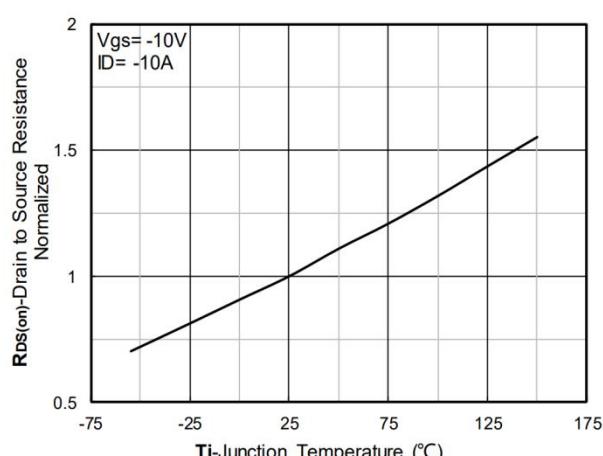


Figure 6. Normalized On-Resistance

Typical Characteristics

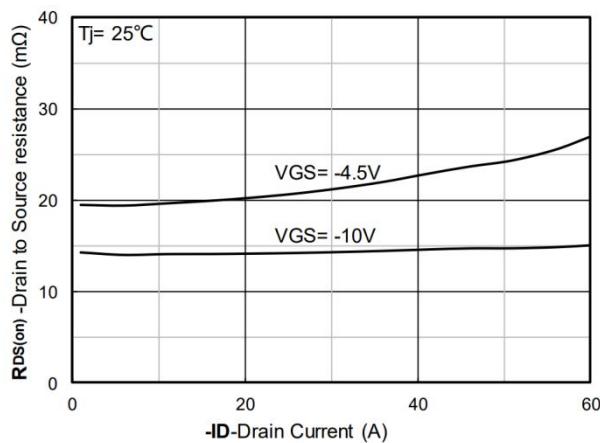


Figure 7. $R_{DS(on)}$ VS Drain Current

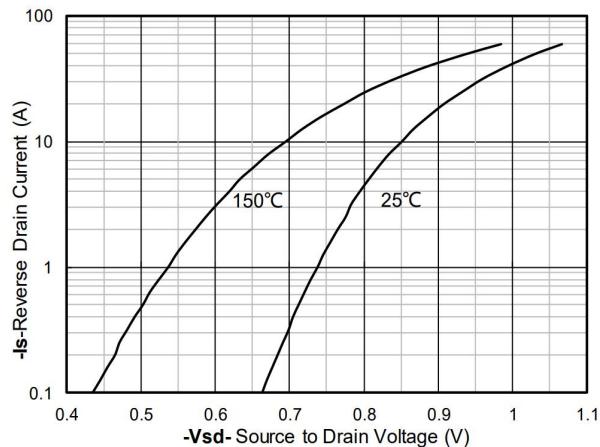


Figure 8. Forward characteristics of reverse diode

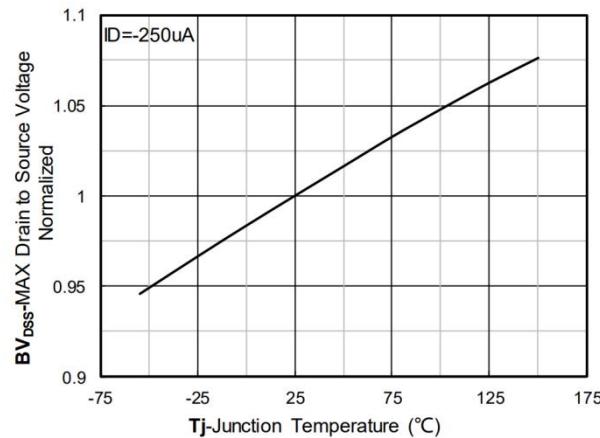


Figure 9. Normalized breakdown voltage

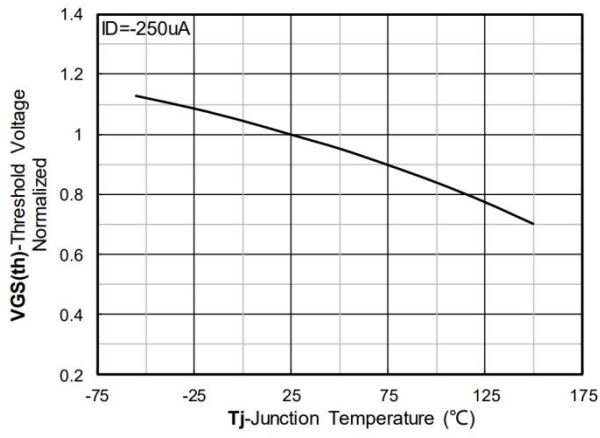


Figure 10. Normalized Threshold voltage

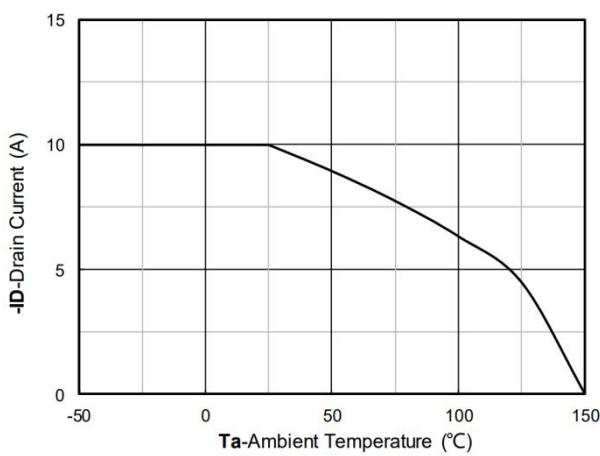


Figure 11. Current dissipation

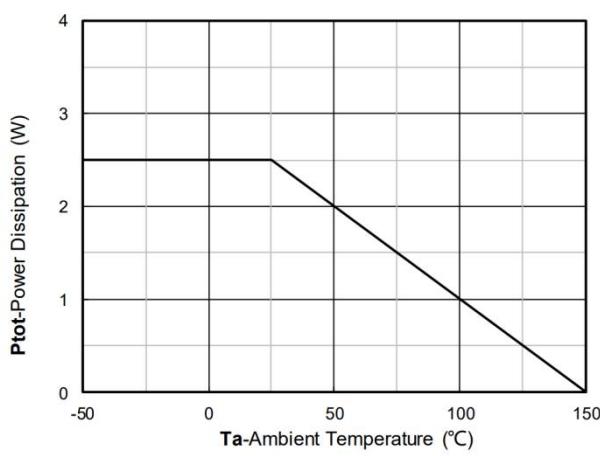


Figure 12. Power dissipation

Typical Characteristics

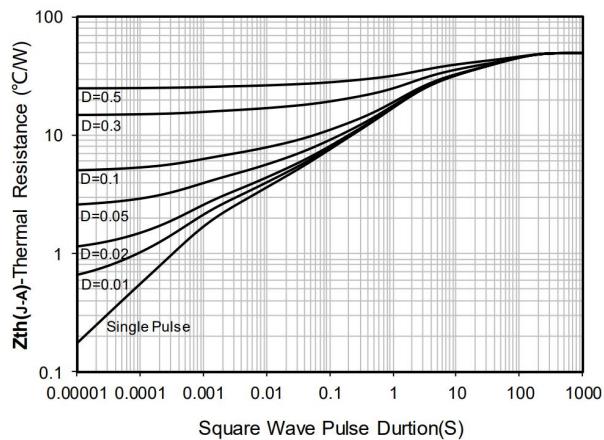


Figure 13. Maximum Transient Thermal Impedance

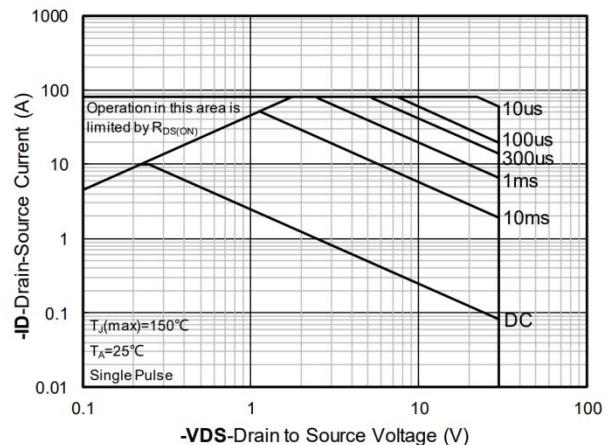
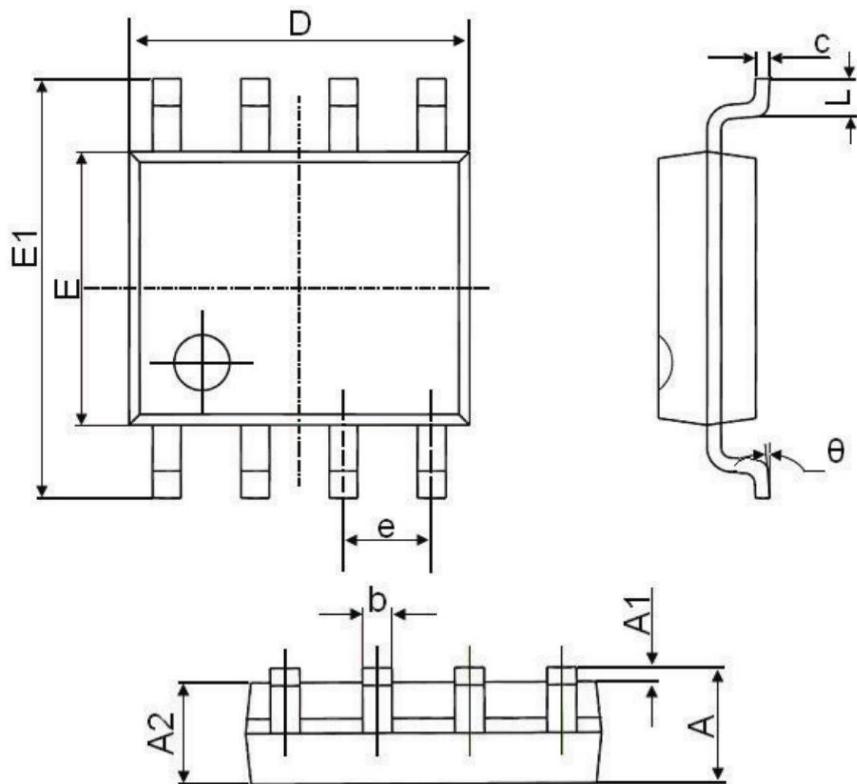


Figure 14. Safe Operation Area

SOP-8 Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.250	1.650	0.049	0.065
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°