

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
-30V	70mΩ@-10V	-3A
	105mΩ@-4.5V	

Feature

- Trench Power LV MOSFET technology
- High density cell design for Low RDS(ON)
- High Speed switching

Application

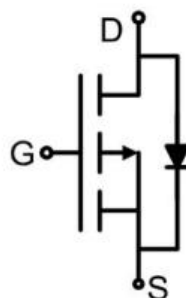
- PWM applications
- Power management
- Load switch

Package

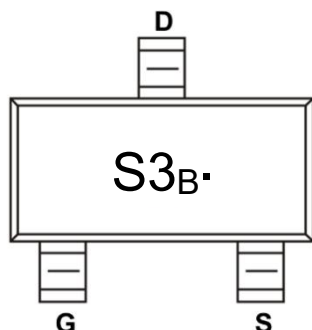


SOT-23

Circuit diagram



Marking



Absolute maximum ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D	-3	A
Pulsed Drain Current(T _A =100°C)	I _D (100°C)	-1.9	A
Pulsed Drain Current ¹⁾	I _{DM}	-20	A
Power Dissipation ²⁾	P _D	0.8	W
Thermal Resistance from Junction to Ambient ³⁾	R _{θJA}	150	°C/W
Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Electrical characteristics (T_J=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =-250μA	-30			V
Zero gate voltage drain current	I _{DSS}	V _{DS} =-30V,V _{GS} = 0V			-1	μA
		V _{DS} =-30V,V _{GS} = 0V,T _J =150°C			-100	
Gate-body leakage current	I _{GSS}	V _{GS} =±20V, V _{DS} = 0V			±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1.0	-1.5	-2.4	V
Drain-source on-resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-3A		54	70	mΩ
		V _{GS} =-4.5V, I _D =-2A		80	105	
Dynamic characteristics ⁴⁾						
Input Capacitance	C _{iss}	V _{DS} =-15V,V _{GS} =0V,f =1MHz		260		pF
Output Capacitance	C _{oss}			50		
Reverse Transfer Capacitance	C _{rss}			40		
Gate Charge	Q _g	V _{DS} =-15V,V _{GS} =-10V,I _D =-3A		7		nC
Gate-Source Charge	Q _{gs}			2		
Gate-Drain Charge	Q _{gd}			1		
Gate resistance	R _g	f =1.0MHz,Open drain		18		Ω
Turn-on delay time	t _{d(on)}	V _{DD} =-15V, V _{GS} =-10V, I _D =-3A, R _{GEN} =3Ω		5		nS
Turn-on rise time	t _r			23		
Turn-off delay time	t _{d(off)}			21		
Turn-off fall time	t _f			30		
Source-Drain Diode characteristics						
Diode Forward Current	I _S				-3	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-3A			-1.2	V
Reverse Recovery Charge	Q _{rr}	I _F =-3A,di/dt=100A/μs		4		nC
Reverse Recovery Charge	T _{rr}			12		nS

Notes:

- 1) Repetitive rating; pulse width limited by max. junction temperature.
- 2) P_d is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.
- 3) The value of R_{θJA} is measured with the device mounted on 1 in2 FR-4 board with 2oz. Copper, in the still air environment with T_A = 25°C. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 4) Guaranteed by design, not subject to production testing.

Typical Characteristics

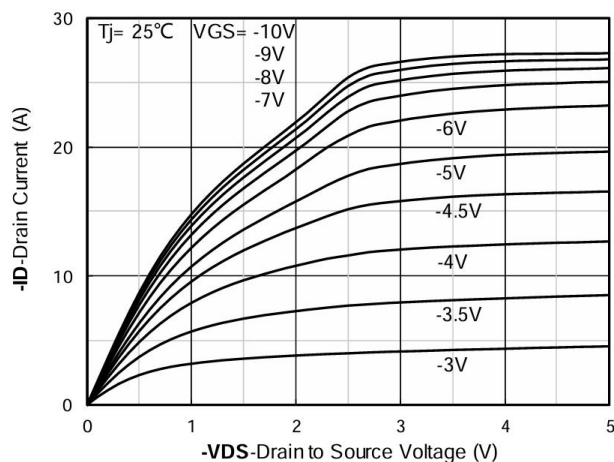


Figure 1. Output Characteristics

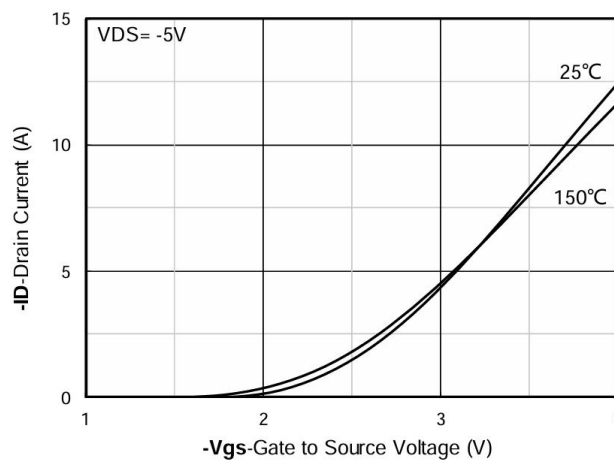


Figure 2. Transfer Characteristics

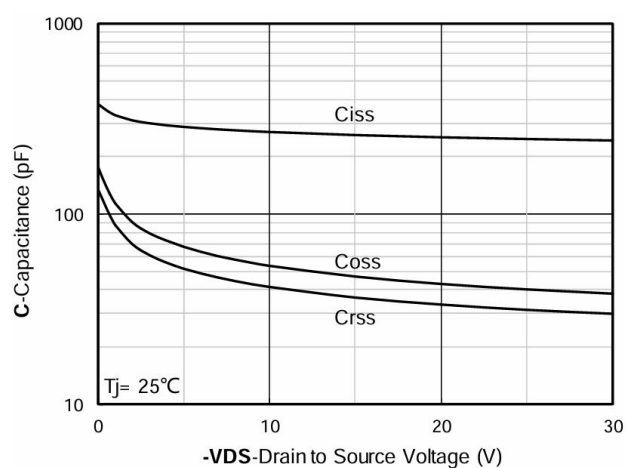


Figure 3. Capacitance Characteristics

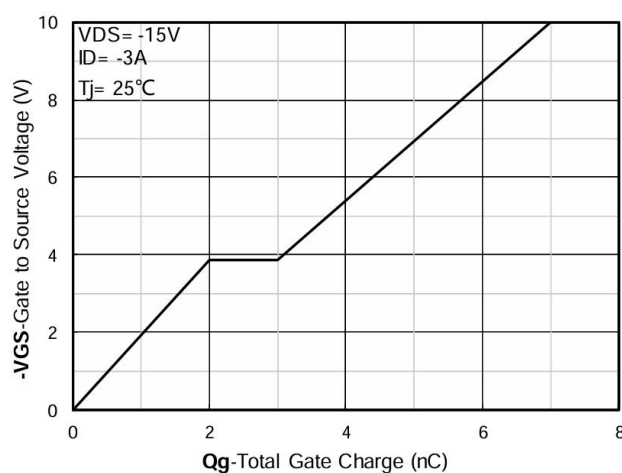


Figure 4. Gate Charge

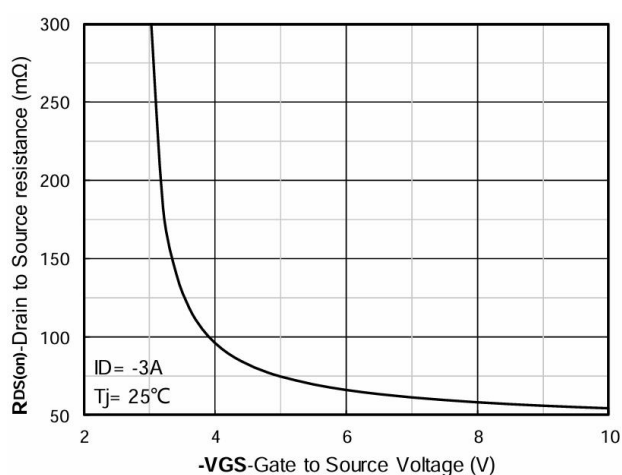


Figure 5. On-Resistance vs Gate to Source Voltage

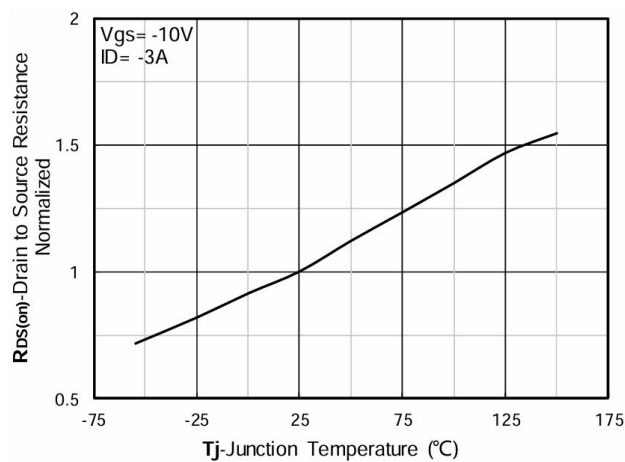


Figure 6. Normalized On-Resistance

Typical Characteristics

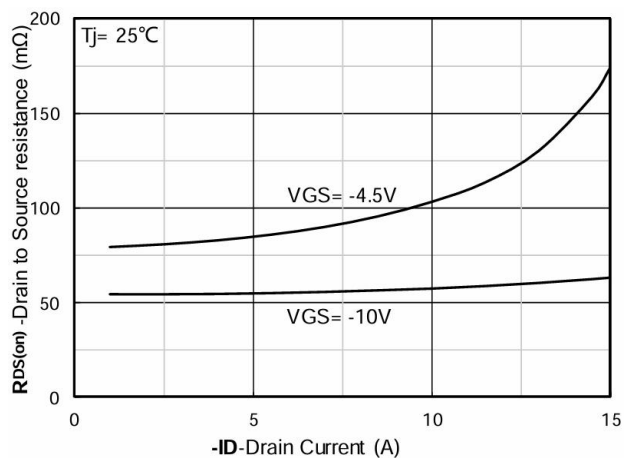


Figure 7. RDS(on) VS Drain Current

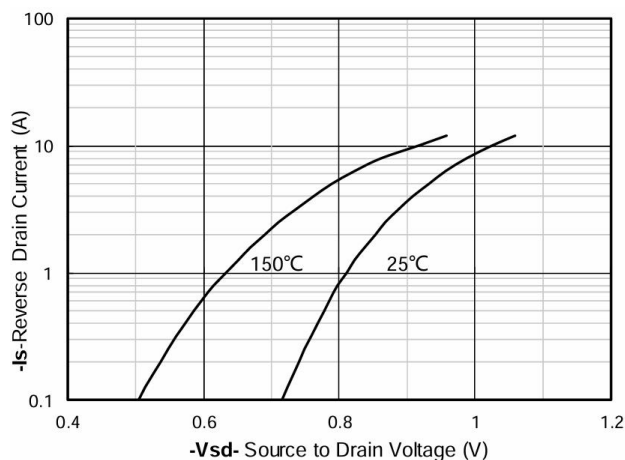


Figure 8. Forward characteristics of reverse diode

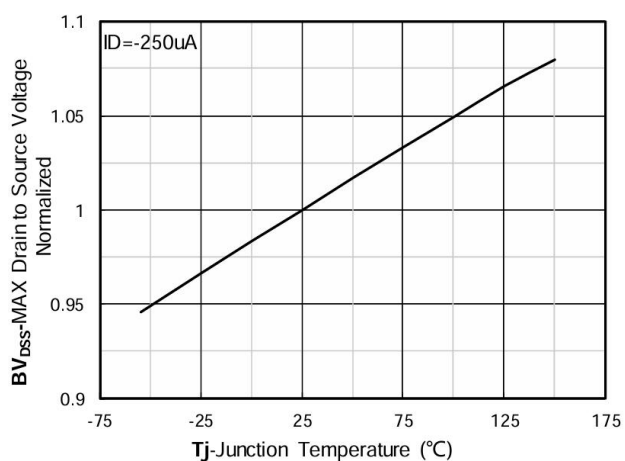


Figure 9. Normalized breakdown voltage

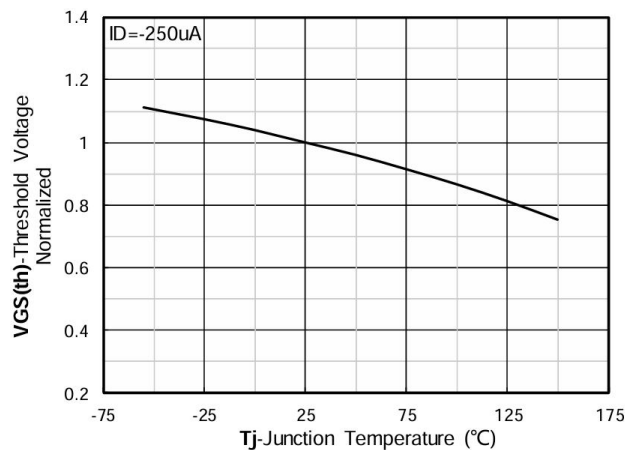


Figure 10. Normalized Threshold voltage

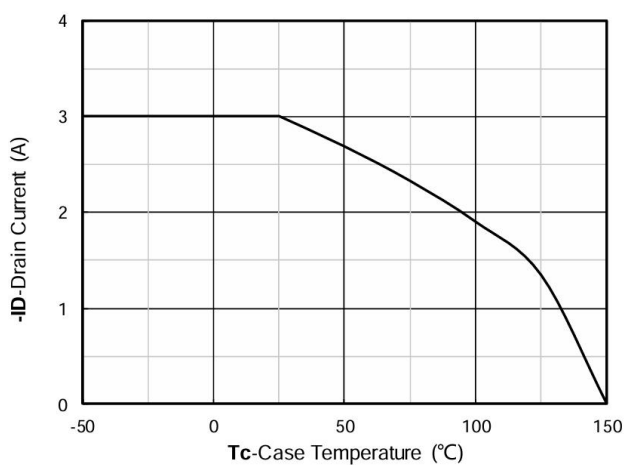


Figure 11. Current dissipation

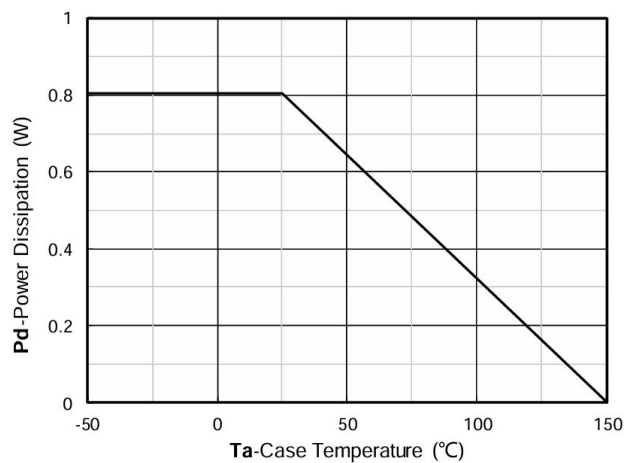


Figure 12. Power dissipation

Typical Characteristics

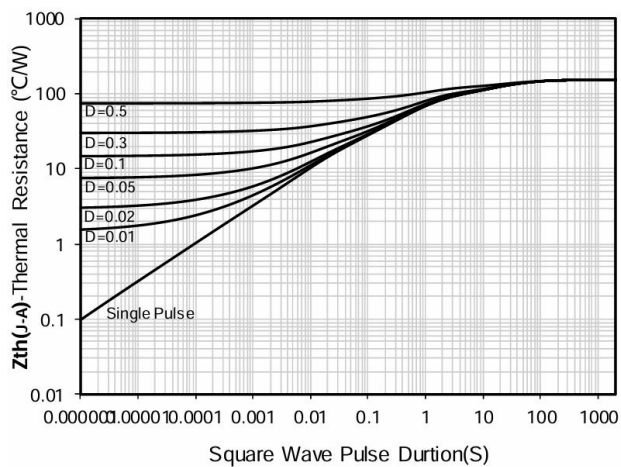


Figure 13. Maximum Transient Thermal Impedance

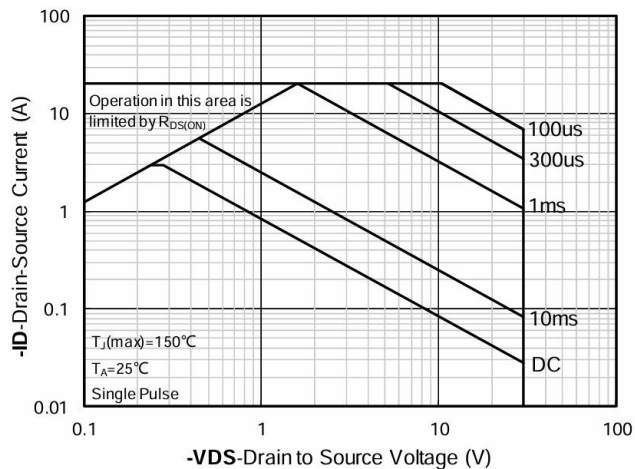
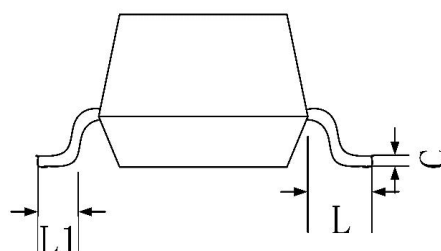
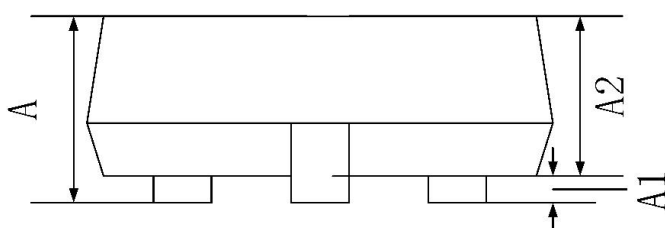
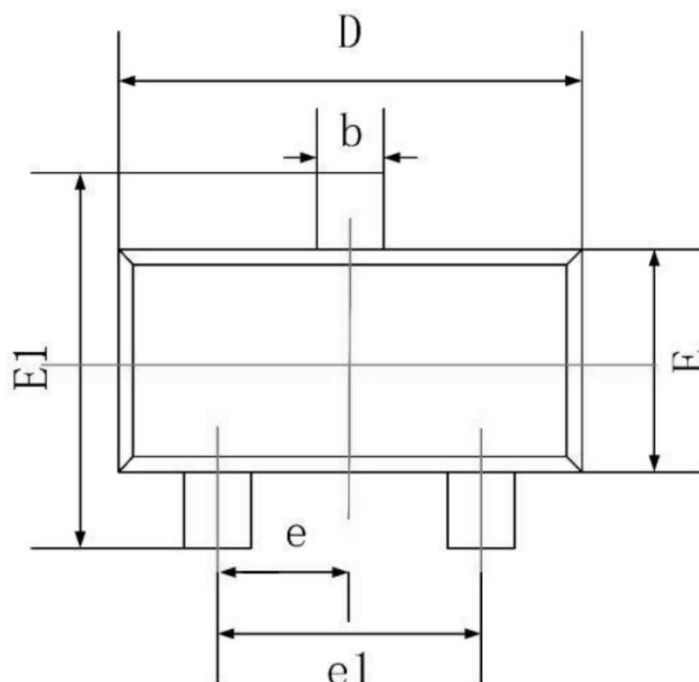


Figure 14. Safe Operation Area

SOT-23 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.890	1.200	0.035	0.047
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.510	0.012	0.020
c	0.080	0.200	0.003	0.008
D	2.800	3.040	0.110	0.120
E	1.200	1.400	0.047	0.055
E1	2.200	2.600	0.087	0.102
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.200	0.500	0.008	0.020