

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
600V	$11\Omega@10V$	1A

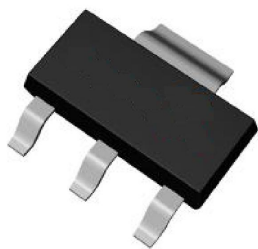
Feature

- Self-aligned planar technology
- Low conduction losses

Application

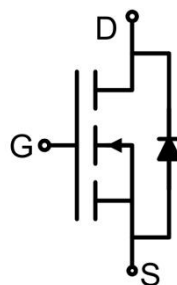
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)

Package



SOT-223

Circuit diagram



Marking



Absolute maximum ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	600	V
Gate-Source Voltage	V _{GS}	±30	V
Continuous Drain Current	I _D	1	A
Continuous Drain Current (T _C =100°C)	I _D	0.6	A
Pulsed Drain Current ¹⁾	I _{DM}	4	A
Single Pulse Avalanche Energy ²⁾	E _{AS}	14	mJ
Power Dissipation (T _L =25°C)	P _D	20	W
Thermal Resistance Junction to Case	R _{θJC}	6.25	°C/W
Operating Junction Temperature	T _J	-55 ~ +150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Electrical characteristics (T_A=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	600			V
Zero gate voltage drain current	I _{DSS}	V _{DS} = 600V, V _{GS} = 0V, T _J = 25°C			25	μA
Gate-body leakage current	I _{GSS}	V _{DS} = 0V, V _{GS} = ±30V			±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2		4	V
Drain-source on-resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 0.5A		8.5	11	Ω
Dynamic characteristics³⁾						
Input Capacitance	C _{iss}	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz		150		pF
Output Capacitance	C _{oss}			25		
Reverse Transfer Capacitance	C _{rss}			5.4		
Total Gate Charge	Q _g	V _{DS} = 480V, V _{GS} = 10V, I _D = 1A		4.8		nC
Gate-Source Charge	Q _{gs}			0.7		
Gate-Drain Charge	Q _{gd}			2.7		
Turn-off delay time	t _{d(off)}	V _{DS} = 300V, I _D = 1A, R _G = 25Ω		13		nS
Source-Drain Diode characteristics						
Diode Forward Current	I _S				1	A
Diode Forward voltage	V _{SD}	V _{GS} = 0V, I _S = 0.5A, T _J = 25°C			1.4	V
Reverse Recovery Time	T _{rr}	I _F = 1A, di/dt = 100A/us		190		nS
Reverse Recovery Charge	Q _{rr}	T _J = 25°C		0.53		μC

Notes:

- 1) The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2% .
- 2) The test condition is V_{DD} = 50V, L = 30mH, R_G = 25Ω, I_{AS} = 1A .
- 3) Guaranteed by design, not subject to production.

Typical Characteristics

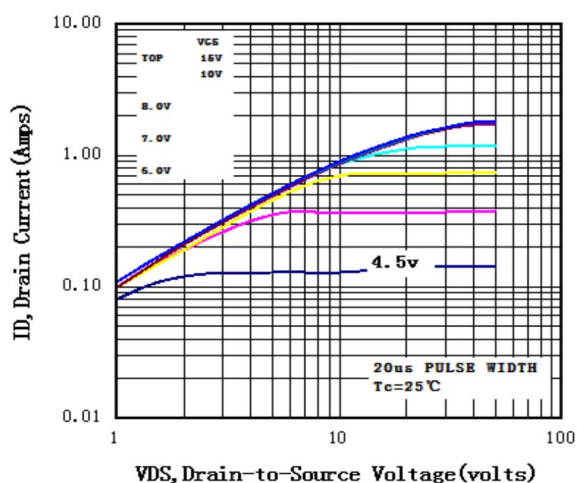


Fig1 Typical Output Characteristics, $T_c = 25^\circ\text{C}$

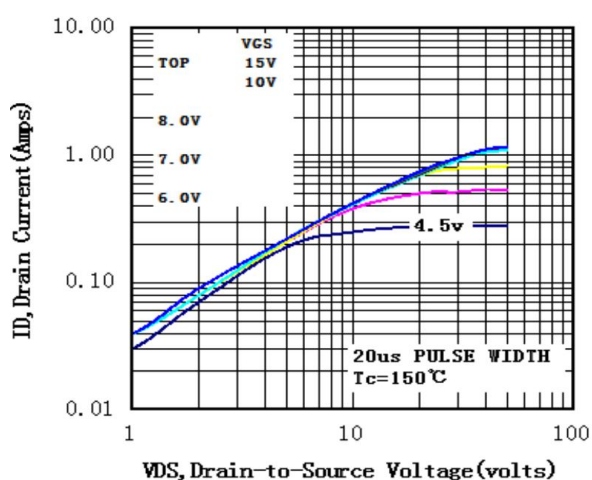


Fig2 Typical Output Characteristics, $T_c = 150^\circ\text{C}$

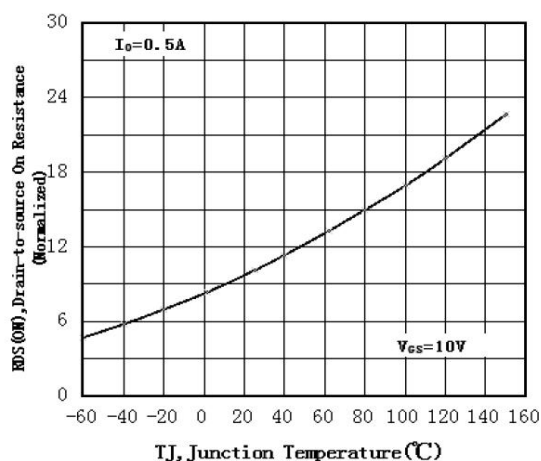


Fig3 Normalized On-Resistance Vs. Temperature

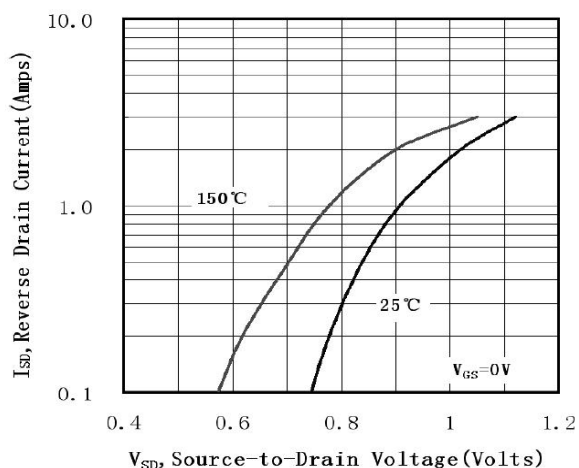


Fig4 Typical Source-Drain Diode Forward Voltage

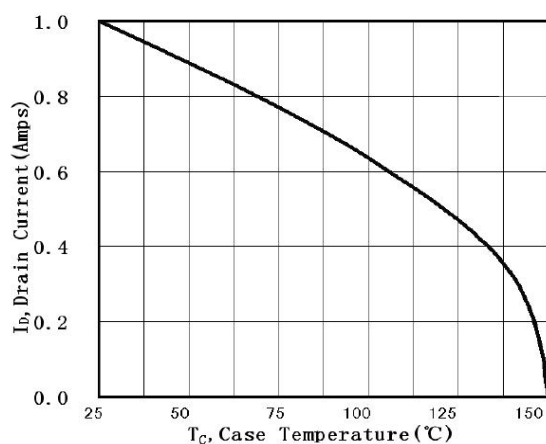


Fig5 Maximum Drain Current Vs. Case Temperature

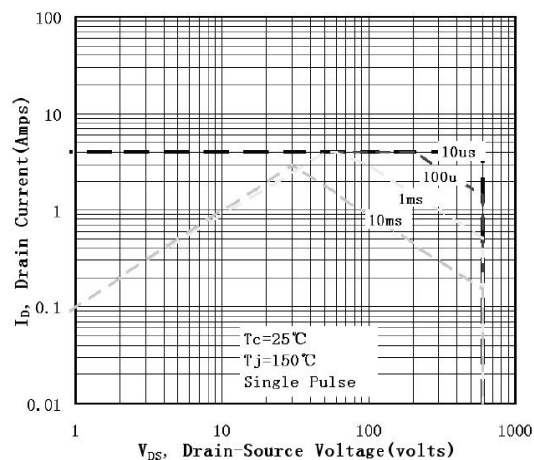


Fig 6 Maximum Safe Operating Area

Typical Characteristics

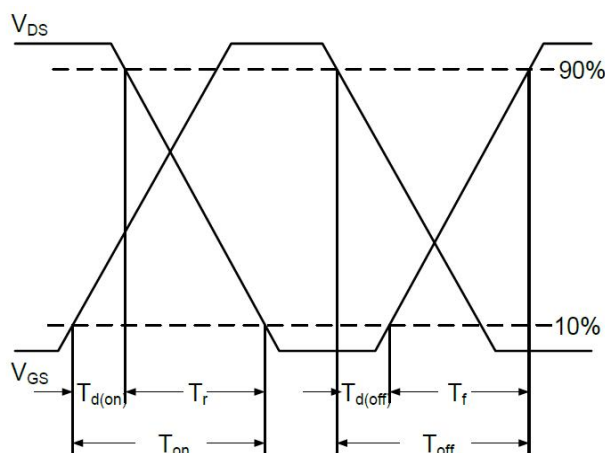


Fig.7 Switching Time Waveform

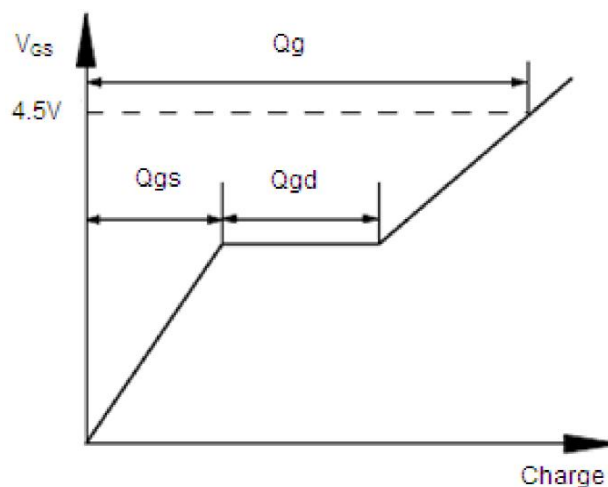
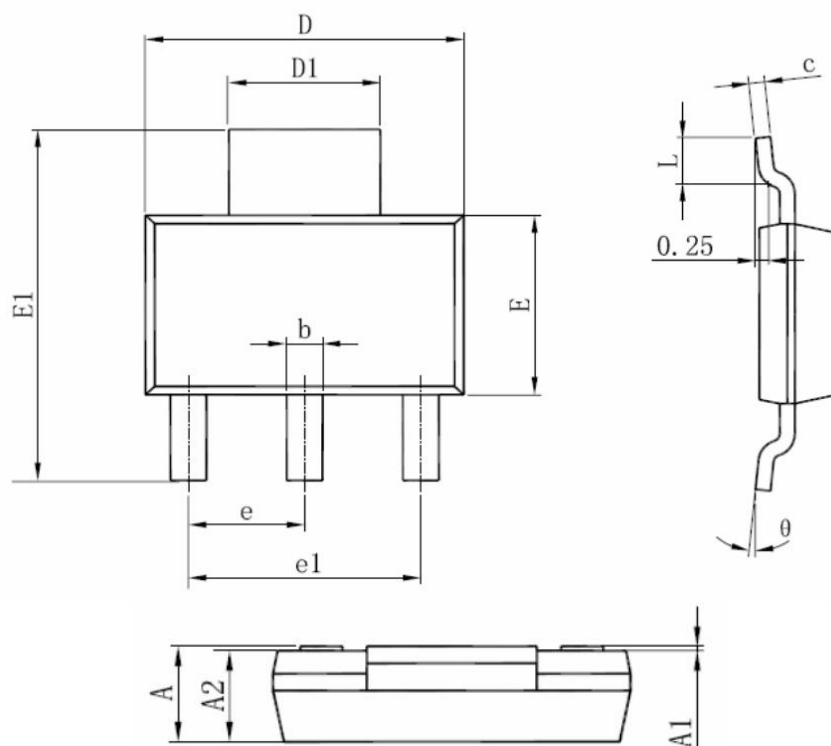


Fig.8 Gate Charge Waveform

SOT-223 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.520	1.800	0.060	0.071
A1	0.000	0.120	0.000	0.005
A2	1.450	1.750	0.057	0.069
b	0.600	0.820	0.024	0.032
c	0.250 BSC.		0.010 BSC.	
D	6.480	6.580	0.255	0.259
D1	2.900	3.100	0.114	0.122
E	3.300	3.700	0.130	0.146
E1	6.800	7.200	0.268	0.283
e	2.300 BSC.		0.091 BSC.	
e1	4.600 BSC.		0.181	
L	0.800	1.200	0.031	0.047
θ	0°	10°	0°	10°