

Product Summary

V _{(BR)DSS}	R _{D(on)MAX}	I _D
100V	3.3mΩ@10V	180A

Feature

- Split gate trench MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low RDS(ON)

Application

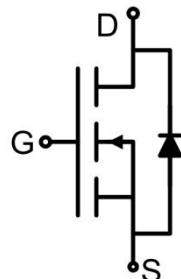
- DC/DC Converter
- Power switching application
- Uninterruptible power supply

Package

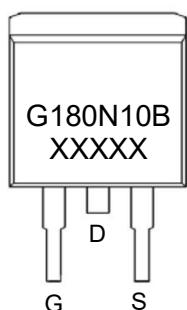


TO-263AB

Circuit diagram



Marking



Absolute maximum ratings (T_A=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current (T _c =25°C)	I _D	180	A
Continuous Drain Current (T _c =100°C)	I _D (100°C)	113	A
Pulsed Drain Current ¹⁾	I _{DM}	720	A
Power Dissipation ³⁾	P _D	2.7	W
Thermal Resistance,Junction-to-Case	R _{θJC}	0.5	°C/W
Single pulse avalanche energy ²⁾	E _{AS}	1024	mJ
Junction Temperature	T _J	150	°C
Storage Temperature	T _{STG}	-55 ~ +150	°C

Electrical characteristics (T_J=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =250μA	100			V
Zero gate voltage drain current	I _{DSS}	V _{DS} =100V, V _{GS} = 0V			1	μA
Gate-body leakage current	I _{GSS}	V _{GS} =±20V, V _{DS} = 0V			±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2	3	4	V
Drain-source on-resistance	R _{DS(on)}	V _{GS} =10V, I _D =50A		2.5	3.3	mΩ
Dynamic characteristics⁴⁾						
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, f =1MHz		8900		pF
Output Capacitance	C _{oss}			2750		
Reverse Transfer Capacitance	C _{rss}			65		
Total Gate Charge	Q _g	V _{DS} =50V, V _{GS} =10V, I _D =90A		122		nC
Gate-Source Charge	Q _{gs}			37		
Gate-Drain Charge	Q _{gd}			29		
Turn-on delay time	t _{d(on)}	V _{DD} =50V, V _{GS} =10V, I _D =90A, R _{GEN} =2.2Ω		25		nS
Turn-on rise time	t _r			194		
Turn-off delay time	t _{d(off)}			52		
Turn-off fall time	t _f			13		
Source-Drain Diode characteristics						
Diode Forward Current	I _S				180	A
Diode Forward voltage	V _{SD}	V _{GS} =0V, I _S =50A			1.2	V
Reverse Recovery Time	t _{rr}	I _F =90A, di/dt = 400A/μs		56		nS
Reverse Recovery Charge	Q _{rr}			317		nC

Notes:

- 1) Repetitive rating; pulse width limited by max. junction temperature.
- 2) T_J=25°C, V_{DD}=50V, V_G=10V, R_G=25Ω, L=2mH, I_{AS}=32A.
- 3) P_d is based on max. junction temperature, using junction-case thermal resistance.
- 4) Guaranteed by design, not subject to production testing.



Typical Characteristics

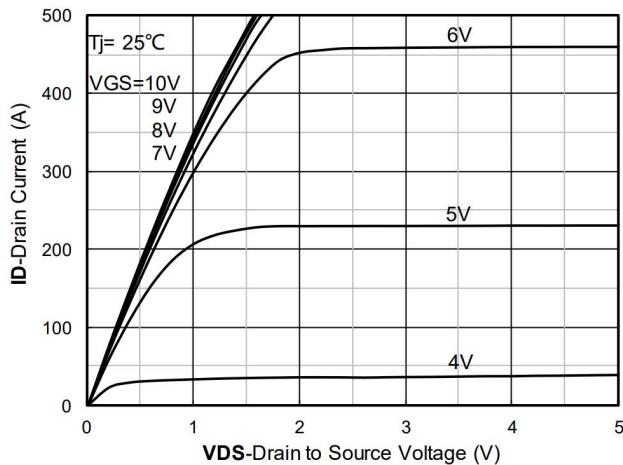


Figure 1. Output Characteristics

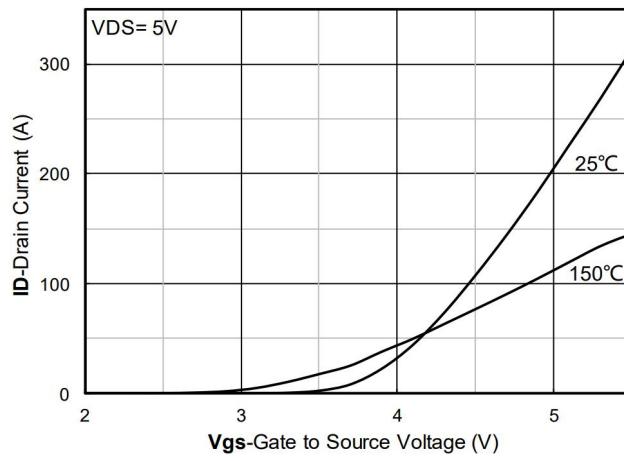


Figure 2. Transfer Characteristics

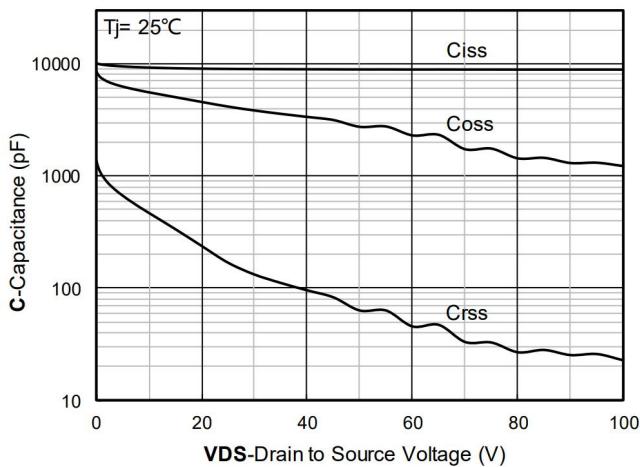


Figure 3. Capacitance Characteristics

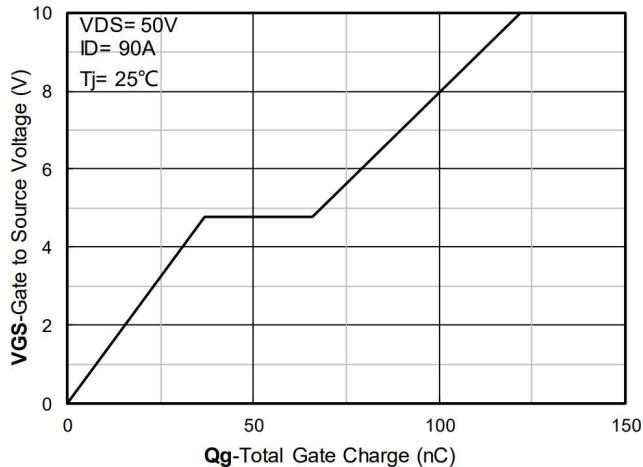


Figure 4. Gate Charge

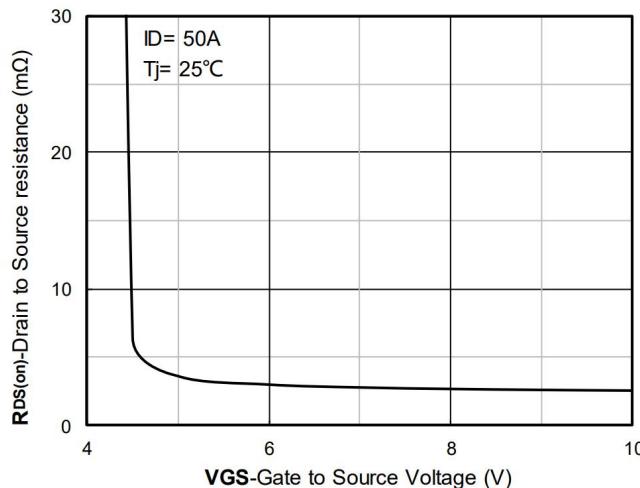


Figure 5. On-Resistance vs Gate to Source Voltage

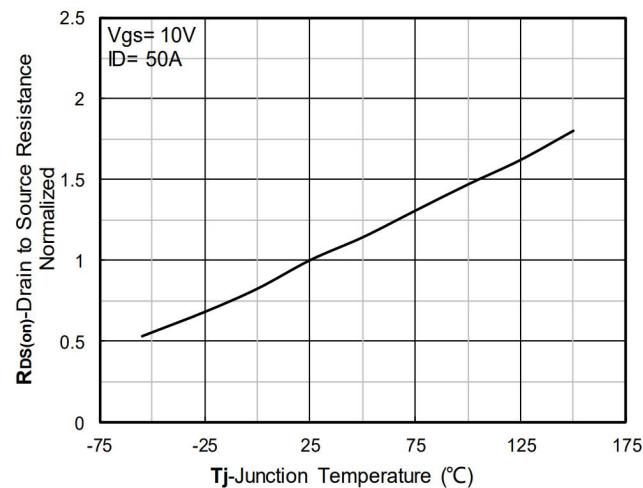


Figure 6. Normalized On-Resistance

Typical Characteristics

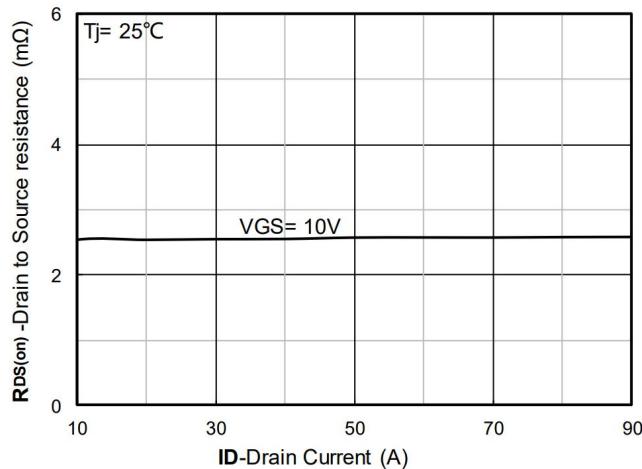


Figure 7. $R_{DS(on)}$ VS Drain Current

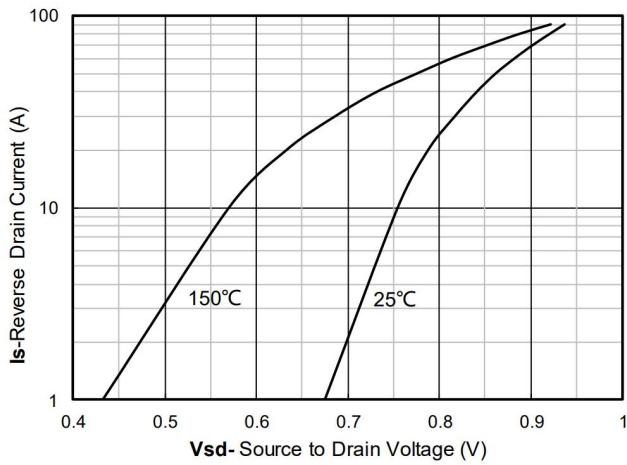


Figure 8. Forward characteristics of reverse diode

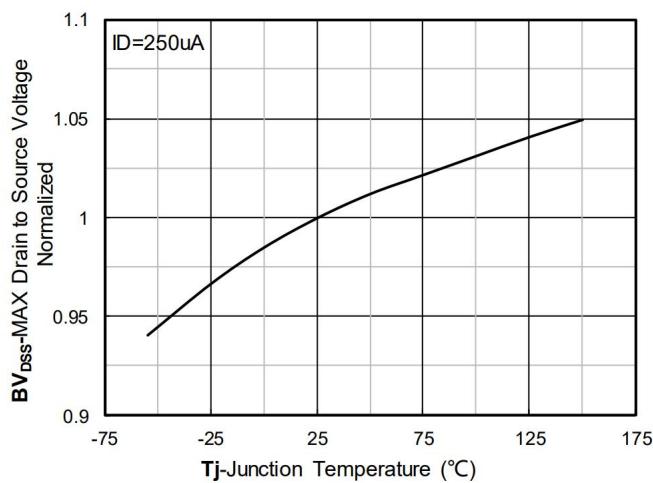


Figure 9. Normalized breakdown voltage

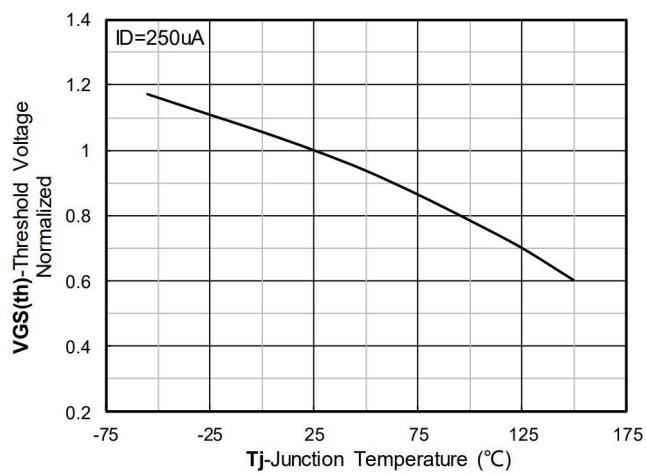


Figure 10. Normalized Threshold voltage

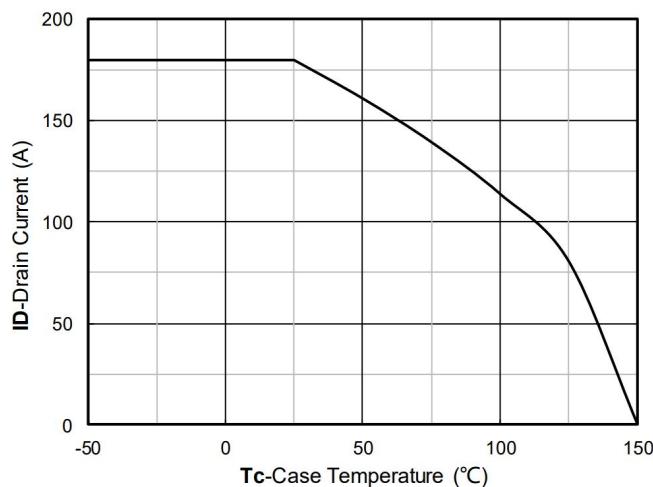


Figure 11. Current dissipation

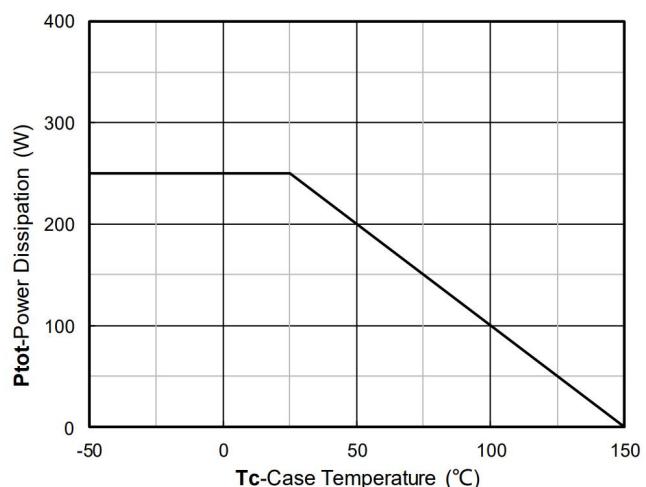


Figure 12. Power dissipation

Typical Characteristics

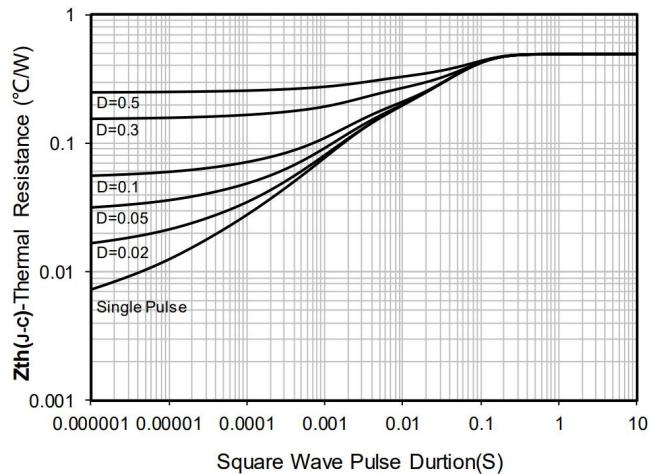


Figure 13. Maximum Transient Thermal Impedance

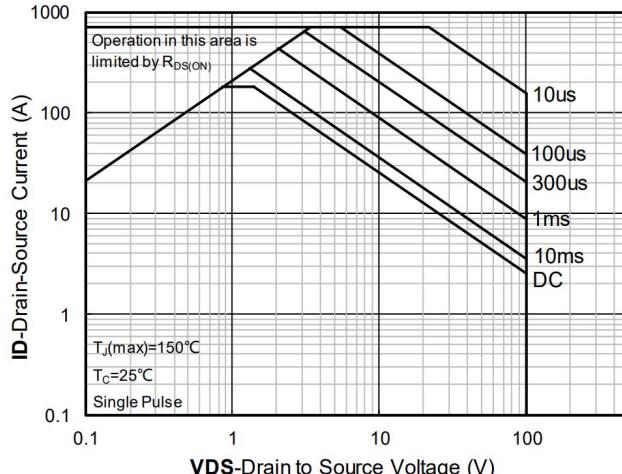
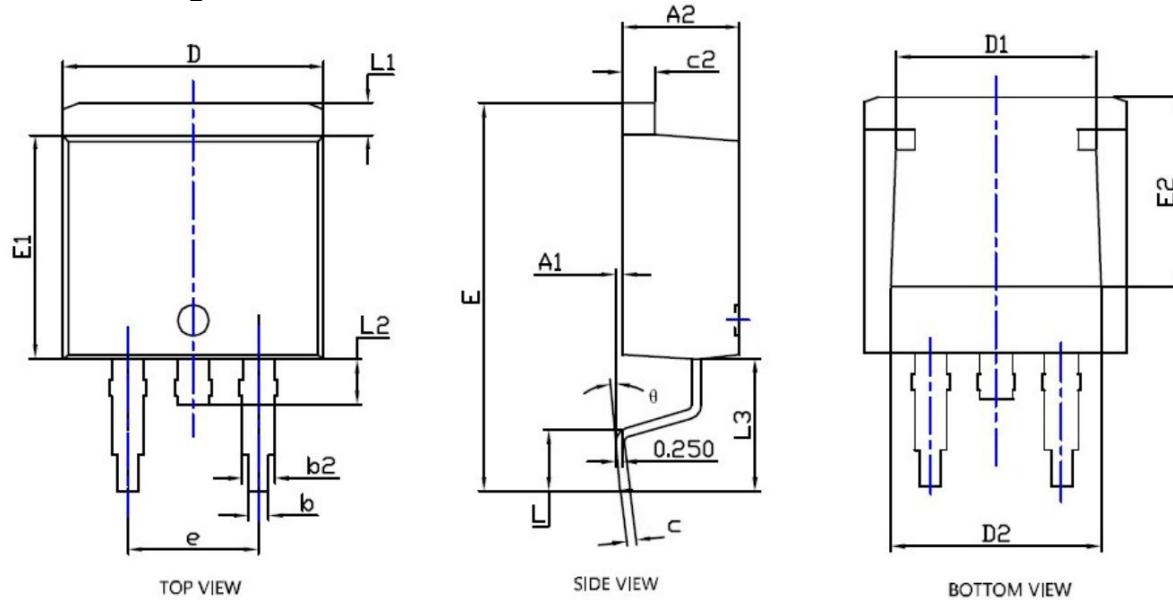


Figure 14. Safe Operation Area

TO-263AB Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A1	0.000	0.250	0.000	0.010
A2	4.430	4.730	0.174	0.186
b	0.720	0.920	0.028	0.036
b2	1.180	1.380	0.046	0.054
c	0.330	0.450	0.013	0.018
c2	1.220	1.340	0.048	0.053
D	10.000	10.300	0.394	0.406
D1	7.500	8.100	0.295	0.319
D2	7.700	8.300	0.303	0.327
E	14.500	15.500	0.571	0.610
E1	8.550	8.850	0.337	0.348
E2	7.000	7.600	0.276	0.299
e	5.080 BSC		0.200 BSC	
L	1.790	2.790	0.070	0.110
L1	1.120	1.420	0.044	0.056
L2	0.770	1.770	0.030	0.070
L3	5.000 REF		0.197 REF	
θ	0°	8°	0°	8°