

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
40V	5.3mΩ@10V	65A
	8mΩ@4.5V	

Feature

- Fast switching speed
- Low gate charge and $R_{DS(on)}$
- Advanced Split Gate Trench technology

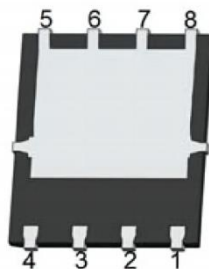
Application

- DC-DC converter
- Motor control
- Portable equipment application

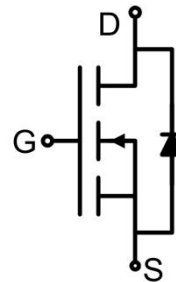
Package



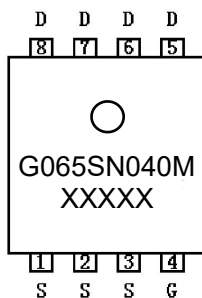
PDFN5*6-8L



Circuit diagram



Marking



Absolute maximum ratings ($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$)	I_D	65	A
Continuous Drain Current ($T_C = 100^\circ\text{C}$)	$I_D(100^\circ\text{C})$	43	A
Pulsed Drain Current	I_{DM}	260	A
Single Pulse Avalanche Energy ¹⁾	E_{AS}	110	mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	65	W
Thermal Resistance Junction to Case	$R_{\theta JC}$	1.92	$^\circ\text{C}/\text{W}$
Operating Junction Temperature	T_J	-55 ~ +150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150	$^\circ\text{C}$

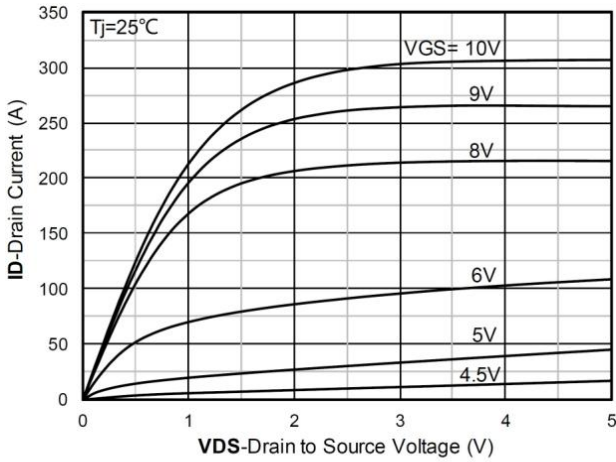
Electrical characteristics ($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	40			V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.1	1.5	2.1	V
Drain-source on-resistance	$R_{DS(on)}$	$V_{GS} = 10\text{V}, I_D = 20\text{A}$		4.3	5.3	m Ω
		$V_{GS} = 4.5\text{V}, I_D = 10\text{A}$		6.2	8	
Dynamic characteristics²⁾						
Input Capacitance	C_{iss}	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$		885		pF
Output Capacitance	C_{oss}			478		
Reverse Transfer Capacitance	C_{rss}			12.1		
Total Gate Charge	Q_g	$V_{DS} = 20\text{V}, V_{GS} = 10\text{V}, I_D = 30\text{A}$		16		nC
Gate-Source Charge	Q_{gs}			4.5		
Gate-Drain Charge	Q_{gd}			5		
Turn-on delay time	$t_{d(on)}$	$V_{DS} = 20\text{V}, V_{GS} = 10\text{V}, I_D = 30\text{A}$ $R_G = 3\Omega$		8		nS
Turn-on rise time	t_r			5		
Turn-off delay time	$t_{d(off)}$			24		
Turn-off fall time	t_f			3.5		
Source-Drain Diode characteristics						
Diode Forward Current	I_S				65	A
Diode Forward voltage	V_{SD}	$V_{GS} = 0\text{V}, I_S = 1\text{A}, T_J = 25^\circ\text{C}$			1.2	V
Reverse recover time	T_{rr}	$I_S = 20\text{A}, di/dt = -100\text{A}/\mu\text{s}$ $T_J = 25^\circ\text{C}$		14		nS
Reverse recovery charge	Q_{rr}			16		nC

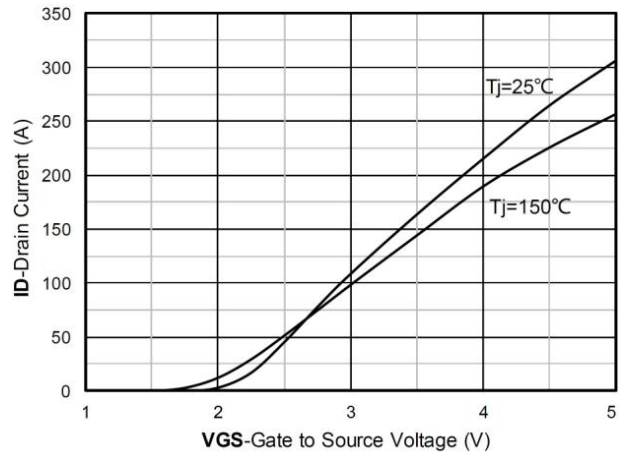
Notes:

- 1) The EAS test condition is $V_{DD} = 20\text{V}, V_{GS} = 10\text{V}, L = 0.5\text{mH}, R_G = 25\Omega$.
- 2) Guaranteed by design, not subject to production.

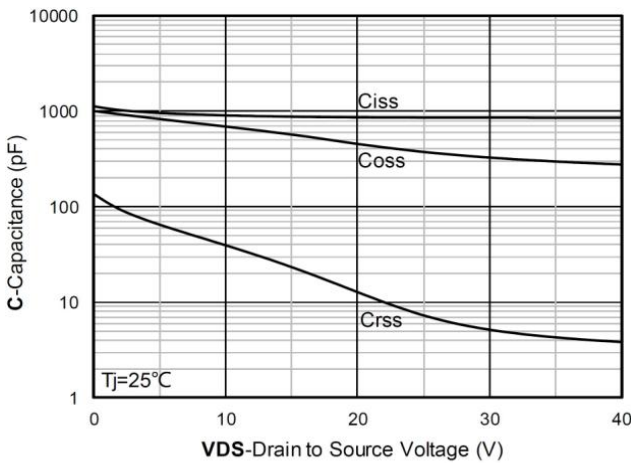
Typical Characteristics



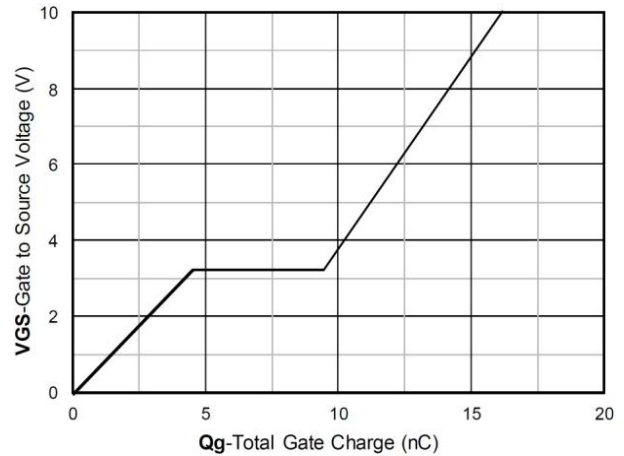
Output Characteristics



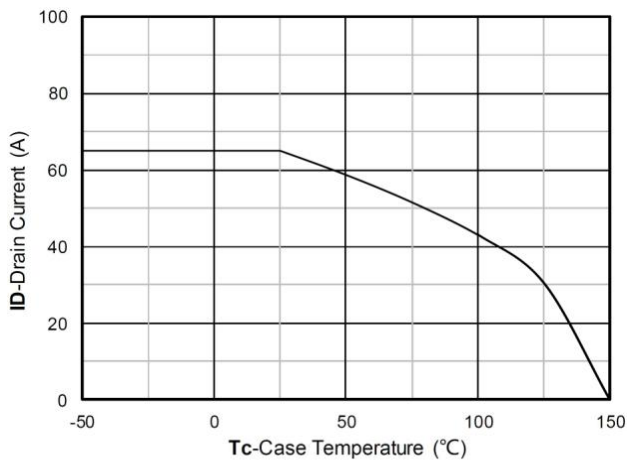
Transfer Characteristics



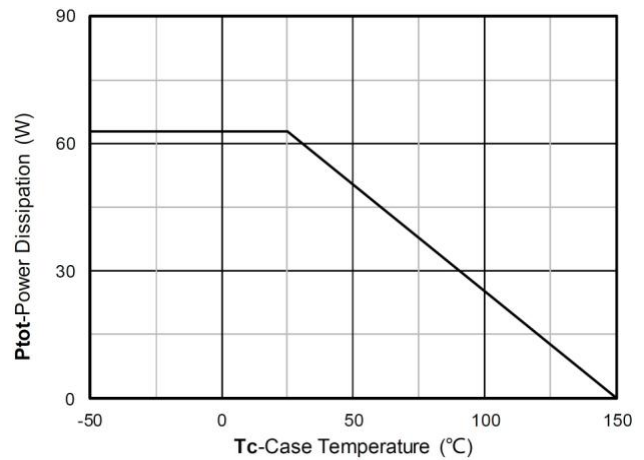
Capacitance Characteristics



Gate Charge

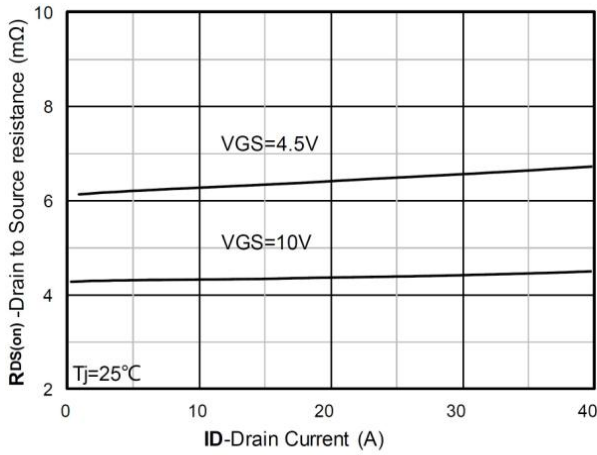


Current dissipation

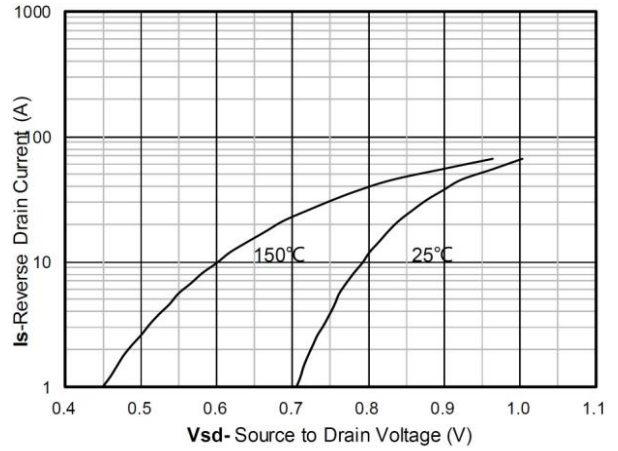


Power dissipation

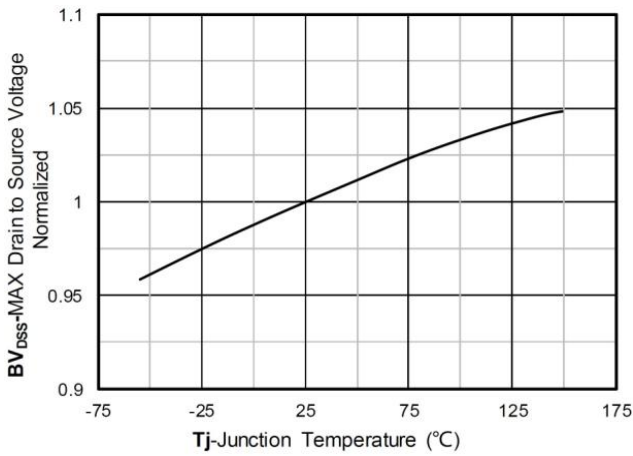
Typical Characteristics



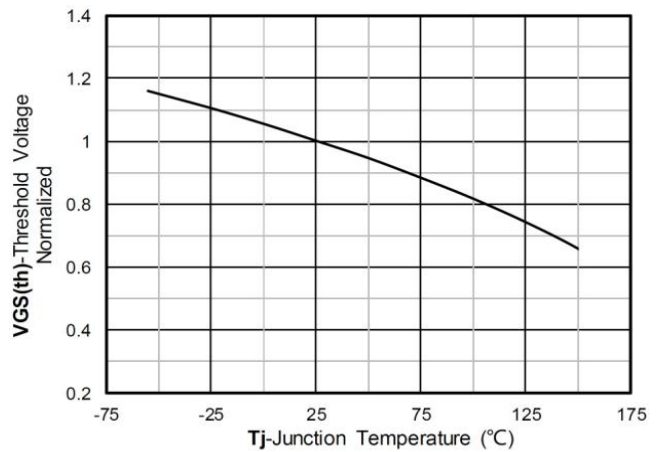
RDS(on) VS Drain Current



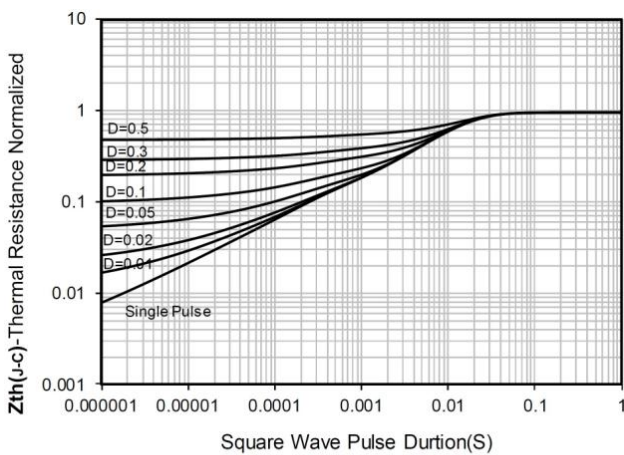
Forward characteristics of reverse diode



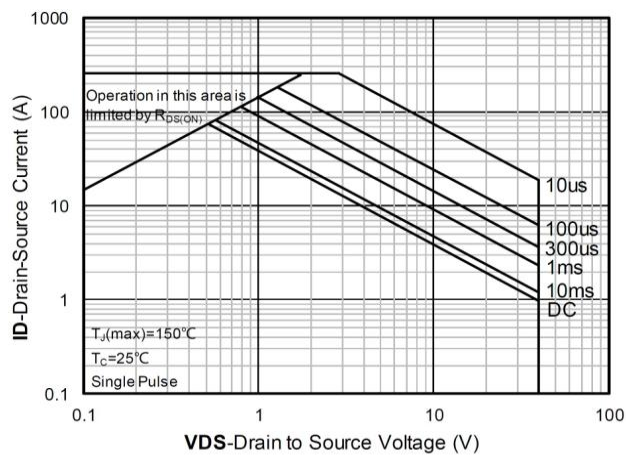
Normalized breakdown voltage



Normalized Threshold voltage

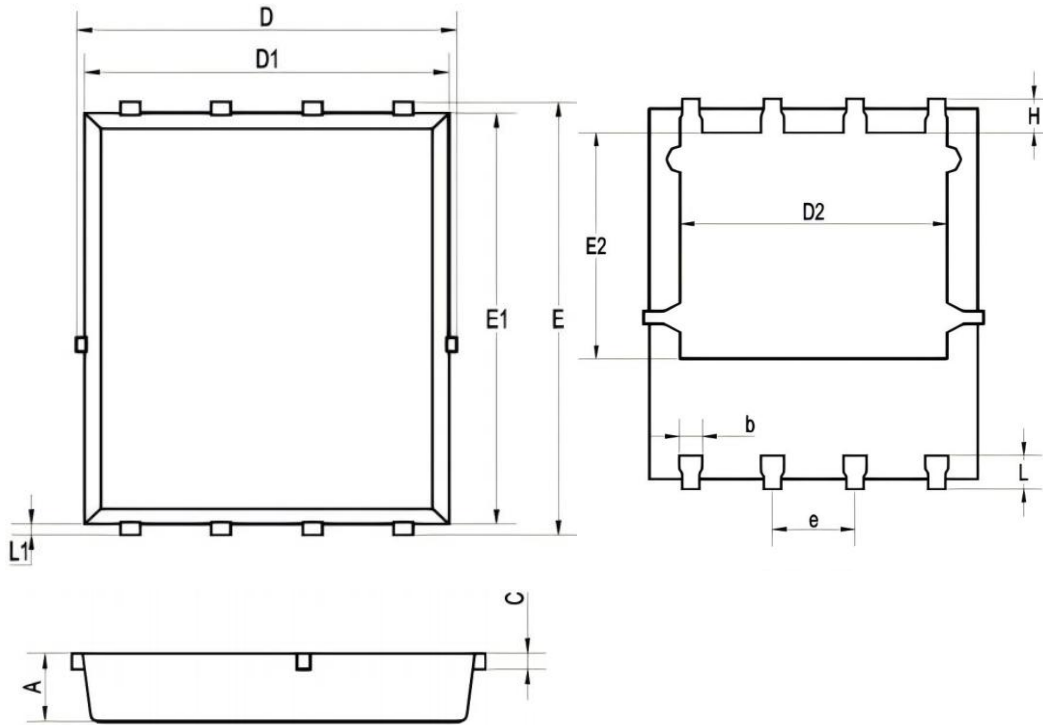


Maximum Transient Thermal Impedance



Safe Operation Area

PDFN5*6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.120	0.035	0.044
b	0.330	0.510	0.013	0.020
C	0.110	0.340	0.004	0.013
D	4.700	5.260	0.185	0.207
D1	4.700	5.100	0.185	0.201
D2	3.560	4.500	0.140	0.177
E	5.750	6.250	0.226	0.246
E1	5.600	6.000	0.220	0.236
E2	3.180	3.660	0.125	0.144
e	1.170	1.370	0.046	0.054
L	0.350	0.710	0.014	0.028
L1	0.060	0.200	0.002	0.008
H	0.350	0.710	0.014	0.028