

## Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	$I_D$
100V	12.5mΩ@10V	45A
	16mΩ@4.5V	

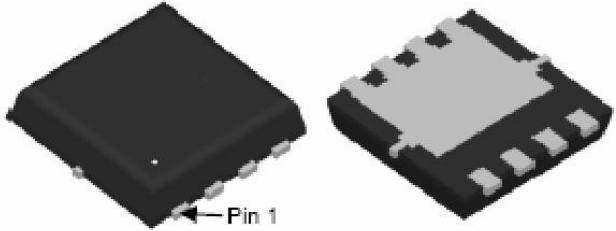
## Feature

- High density cell design for ultra low  $R_{DS(ON)}$
- Split gate trench MOSFET technology
- Excellent package for heat dissipation
- Suffix “-Q1” for AEC-Q101

## Application

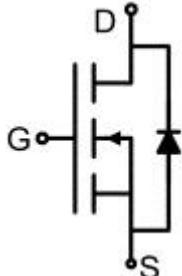
- Power switching application
- Uninterruptible power supply
- DC/DC converter

## Package

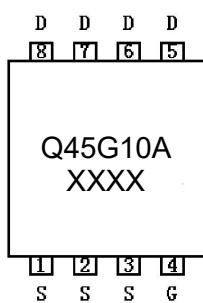


DFN3.3\*3.3-8L

## Circuit diagram



## Marking



**Absolute maximum ratings (T<sub>A</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current (T <sub>C</sub> =25°C )	I <sub>D</sub>	45	A
Continuous Drain Current (T <sub>C</sub> =100 °C )	I <sub>D</sub> (100 °C)	28	A
Pulsed Drain Current <sup>1)</sup>	I <sub>DM</sub>	170	A
Avalanche Energy <sup>2)</sup>	E <sub>AS</sub>	100	mJ
Power Dissipation <sup>3)</sup> (T <sub>C</sub> =25°C )	P <sub>D</sub>	46	W
Thermal Resistance,Junction-to-Ambient <sup>4)</sup>	R <sub>θJA</sub>	60	°C/W
Thermal Resistance,Junction-to-Case	R <sub>θJC</sub>	2.7	°C/W
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>STG</sub>	-55 ~ +150	°C

**Electrical characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA	100			V
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =100V,V <sub>GS</sub> = 0V			1	μA
Gate-body leakage current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> = 0V			±100	nA
Gate threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1	1.7	3	V
Drain-source on-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =22.5A		10	12.5	mΩ
		V <sub>GS</sub> =10V, I <sub>D</sub> =20A		10	12.5	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =20A		12.5	16	
<b>Dynamic characteristics<sup>5)</sup></b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =0V,f =1MHz		1800		pF
Output Capacitance	C <sub>oss</sub>			590		
Reverse Transfer Capacitance	C <sub>rss</sub>			20		
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =50V,V <sub>GS</sub> =10V, I <sub>D</sub> =25A		30		nC
Gate-Source Charge	Q <sub>gs</sub>			9		
Gate-Drain Charge	Q <sub>gd</sub>			4		
Gate Resistance	R <sub>g</sub>	f=1MHz, Open drain		1.4		Ω
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> =50V,V <sub>GS</sub> =10V, I <sub>D</sub> =25A,R <sub>GEN</sub> =3Ω		13		nS
Turn-on rise time	t <sub>r</sub>			52		
Turn-off delay time	t <sub>d(off)</sub>			26		
Turn-off fall time	t <sub>f</sub>			77		
<b>Source-Drain Diode characteristics</b>						
Diode Forward Current	I <sub>S</sub>				45	A
Diode Forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =22.5A			1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =25A,di/dt = 100A/μs		35		nS
Reverse Recovery Charge	Q <sub>rr</sub>			26		nC

Notes:

- 1) Repetitive rating; pulse width limited by max. junction temperature.
- 2) T<sub>J</sub>=25°C, V<sub>G</sub>=10V, R<sub>G</sub>=25Ω, L=0.5mH, I<sub>AS</sub>=20A.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in the still air environment with T<sub>A</sub> =25°C. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
- 5) Guaranteed by design, not subject to production testing.



## Typical Characteristics

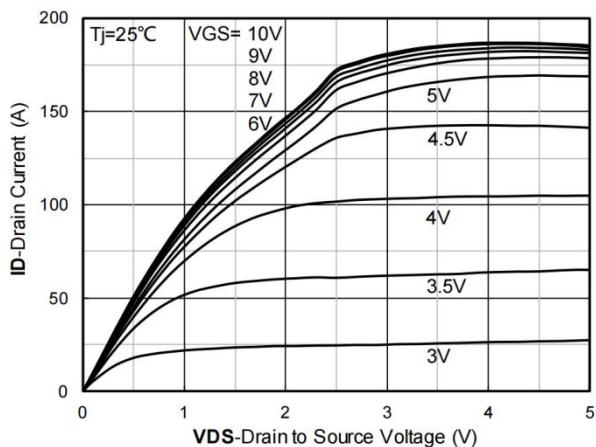


Figure 1. Output Characteristics

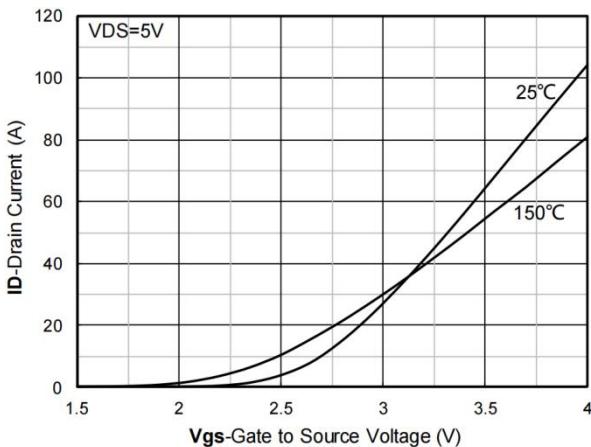


Figure 2. Transfer Characteristics

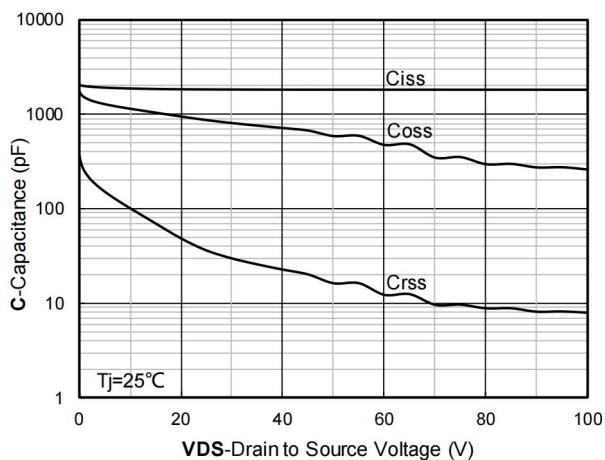


Figure 3. Capacitance Characteristics

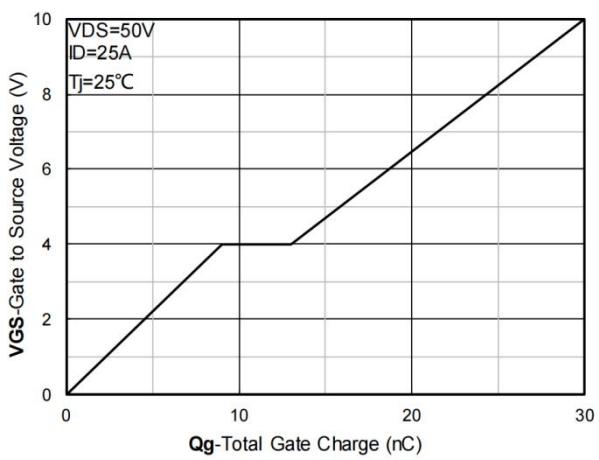


Figure 4. Gate Charge

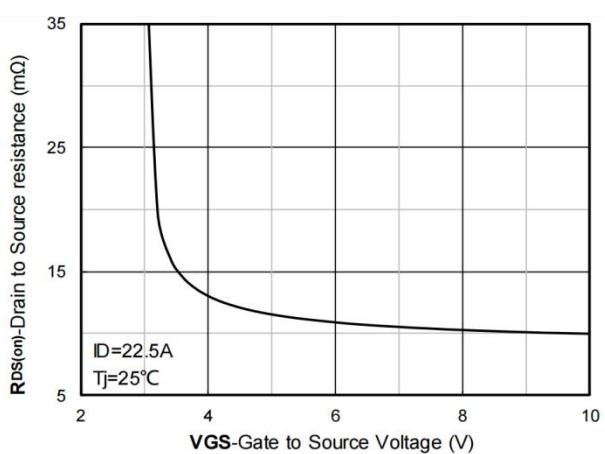


Figure 5. On-Resistance vs Gate to Source Voltage

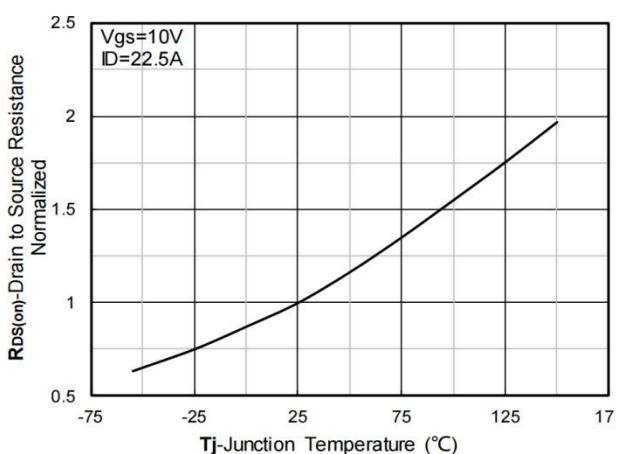


Figure 6. Normalized On-Resistance

## Typical Characteristics

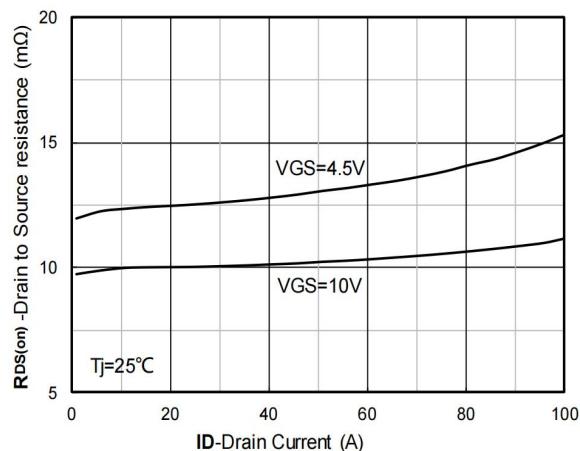


Figure 7. RDS(on) VS Drain Current

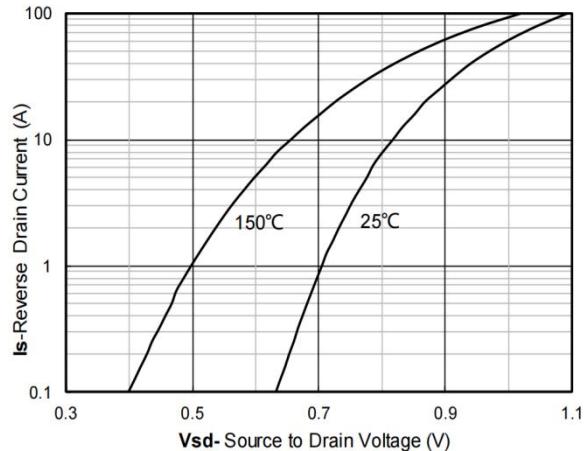


Figure 8. Forward characteristics of reverse diode

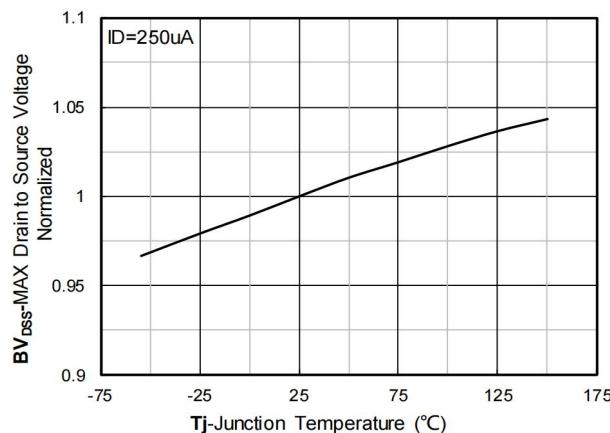


Figure 9. Normalized breakdown voltage

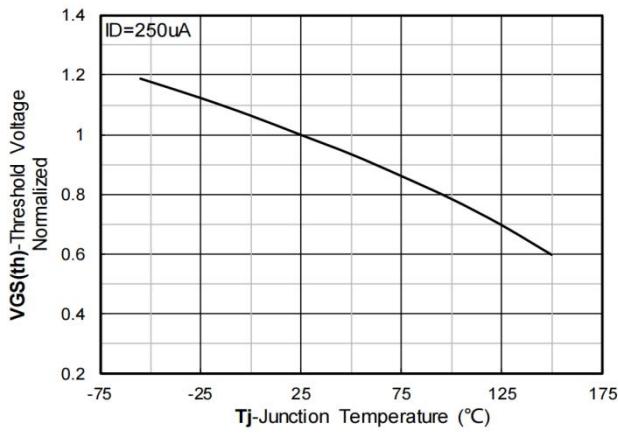


Figure 10. Normalized Threshold voltage

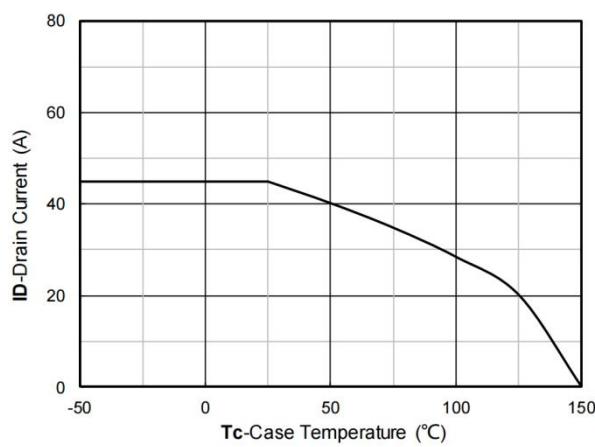


Figure 11. Current dissipation

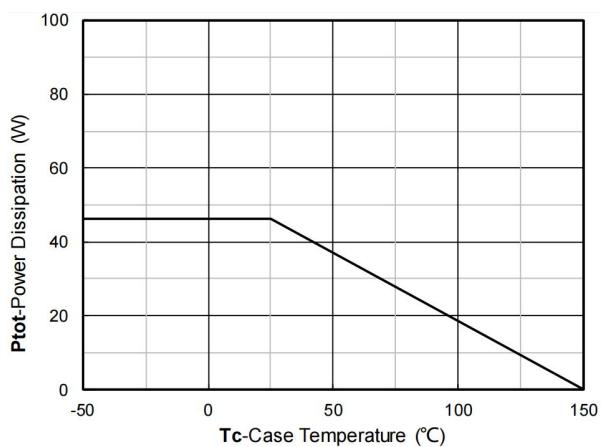


Figure 12. Power dissipation

### Typical Characteristics

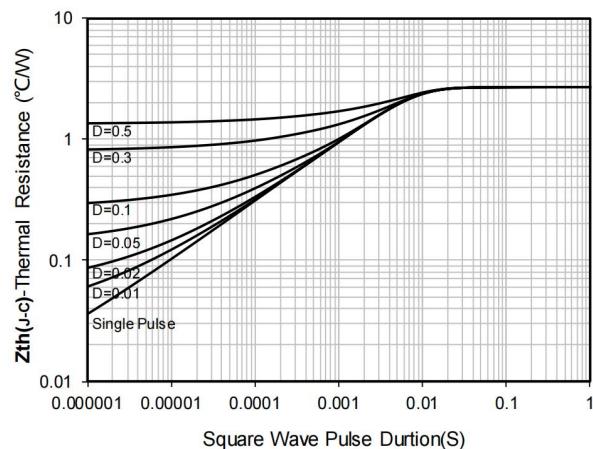


Figure 13. Maximum Transient Thermal Impedance

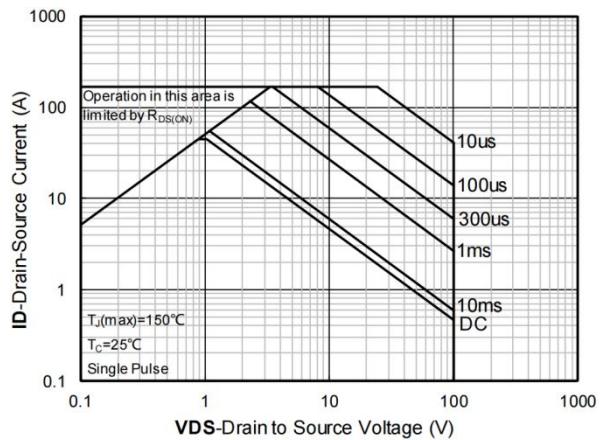
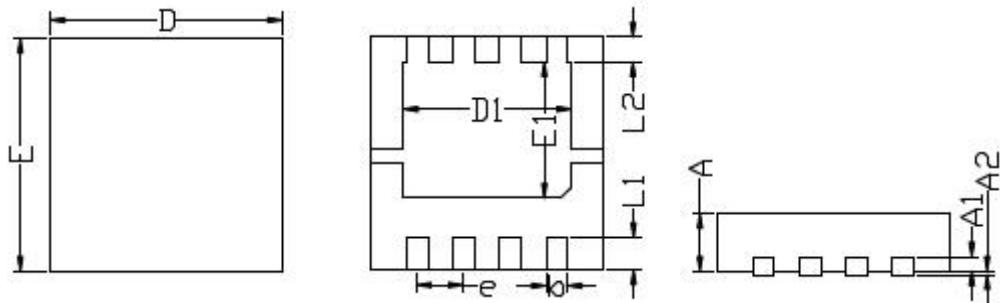


Figure 14. Safe Operation Area

**DFN3.3\*3.3-8L Package Information**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.900	0.027	0.035
A1	0.200BSC		0.008BSC	
A2	0.000	0.100	0.000	0.004
b	0.200	0.400	0.008	0.016
D	3.150	3.350	0.124	0.132
D1	2.200	2.500	0.086	0.098
E	3.150	3.350	0.124	0.132
E1	1.800	2.000	0.071	0.079
e	0.650 BSC		0.026 BSC	
L1	0.350	0.550	0.013	0.022
L2	0.350 BSC		0.014BSC	