AG005SP060MQSG-Q1

P-Channel Enhancement Mode MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
-60V	55mΩ@-10V	-5A
	70mΩ@-4.5V	

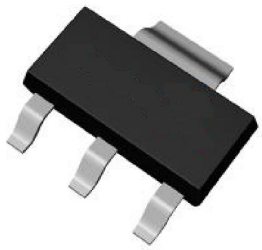
Feature

- Split gate trench MOSFET technology
- Extremely low switching loss
- Excellent stability and uniformity
- Suffix “-Q1” for AEC-Q101

Application

- Power management
- Load switch

Package



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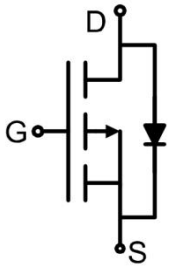
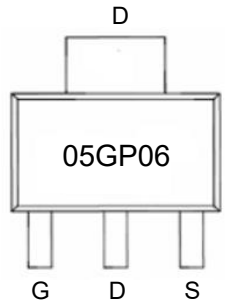
Circuit diagram



Marking



Absolute maximum ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	-5	A
Continuous Drain Current (T_A=100℃)	I_D(100℃)	-3	A
Pulsed Drain Current[1)]	I_{DM}	-25	A
Power Dissipation[2)]	P_D	1	W
Thermal Resistance from Junction to Ambient[3)]	$R_{\theta JA}$	120	℃/W
Junction Temperature	T_J	150	℃
Storage Temperature	T_{STG}	-55 ~ +150	℃

Electrical characteristics (T_J=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D =-250μA	-60			V
Zero gate voltage drain current	I_{DSS}	V_{DS} =-60V,V_{GS} = 0V			-1	μA
Gate-body leakage current	I_{GSS}	V_{GS} =±20V, V_{DS} = 0V			±100	nA
Gate threshold voltage	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =-250μA	-1.5	-2	-3	V
Drain-source on-resistance	$R_{DS(on)}$	V_{GS} =-10V, I_D =-5A		40	55	mΩ
		V_{GS} =-4.5V, I_D =-4A		50	70	
Dynamic characteristics[4)]						
Input Capacitance	C_{iss}	V_{DS} =-30V,V_{GS} =0V,f =1MHz		1050		pF
Output Capacitance	C_{oss}			380		
Reverse Transfer Capacitance	C_{rss}			20		
Total Gate Charge	Q_g	V_{DS} =-30V,V_{GS} =-10V, I_D =-5A		18.7		nC
Gate-Source Charge	Q_{gs}			4.7		
Gate-Drain Charge	Q_{gd}			3		
Turn-on delay time	$t_{d(on)}$	V_{DD}=-30V,V_{GS}=-10V, I_D =-5A,R_{GEN}=2.2Ω		7.5		nS
Turn-on rise time	t_r			40		
Turn-off delay time	$t_{d(off)}$			43		
Turn-off fall time	t_f			55		
Source-Drain Diode characteristics						
Diode Continuous Current	I_S				-5	A
Diode Forward voltage	V_{SD}	V_{GS} =0V, I_S=-5A			-1.2	V
Reverse recover time	t_{rr}	I_F=-5A,di/dt=100A/us		20		nS
Reverse recovery charge	Q_{rr}			8		nC

Notes:

1) Repetitive rating; pulse width limited by max. junction temperature.

2) P_d is based on max. junction temperature, using junction-case and junction-ambient thermal resistance.

3) The value of $R_{\theta JA}$ is measured with the device mounted on the minimum recommend pad size, in the still air environment with T_A =25℃. The maximum allowed junction temperature of 150℃. The value in any given application depends on the user's specific board design.

Typical Characteristics

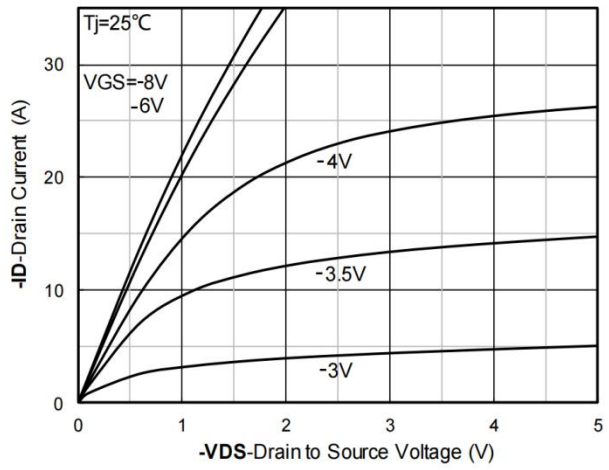


Figure 1. Output Characteristics

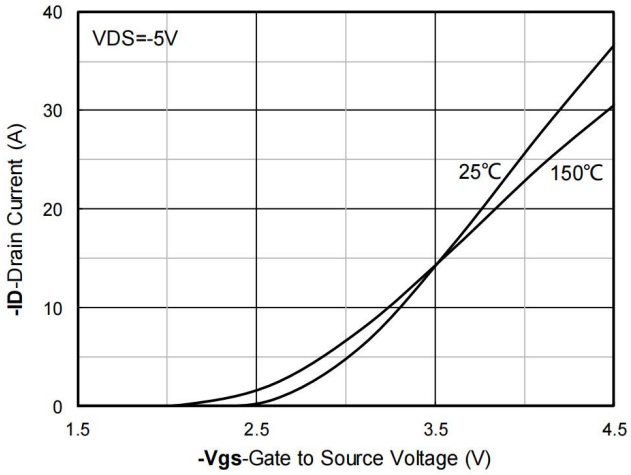


Figure 2. Transfer Characteristics

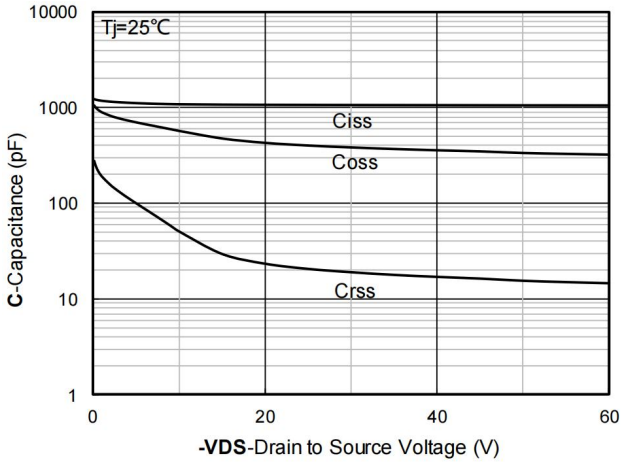


Figure 3. Capacitance Characteristics

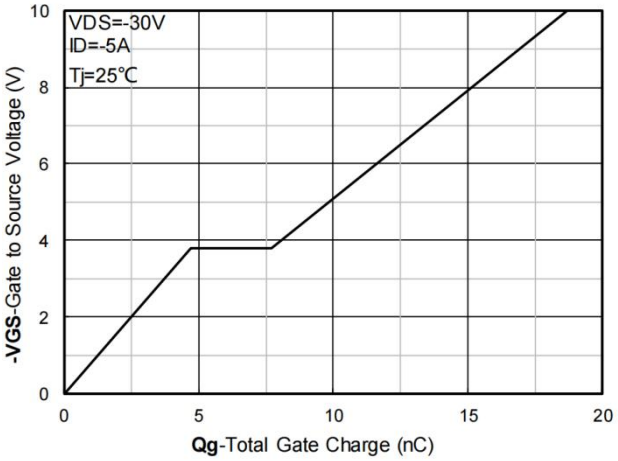


Figure 4. Gate Charge

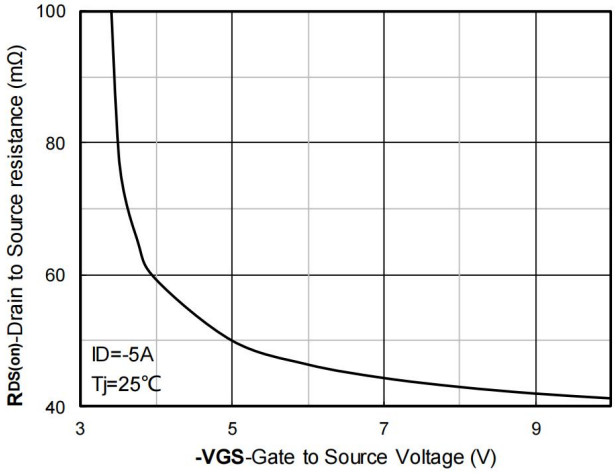


Figure 5. On-Resistance vs Gate to Source Voltage

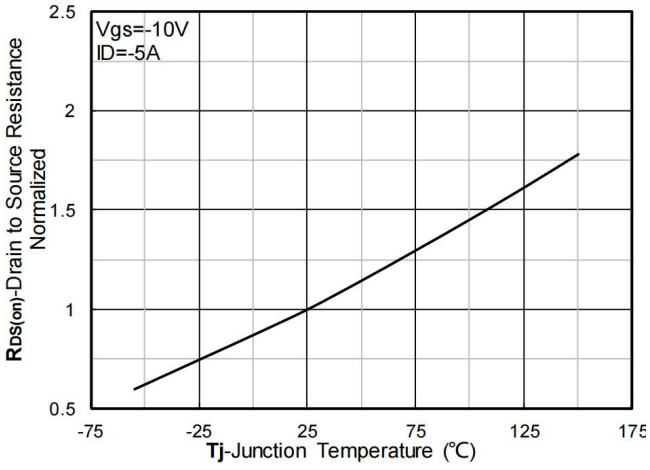


Figure 6. Normalized On-Resistance

Typical Characteristics

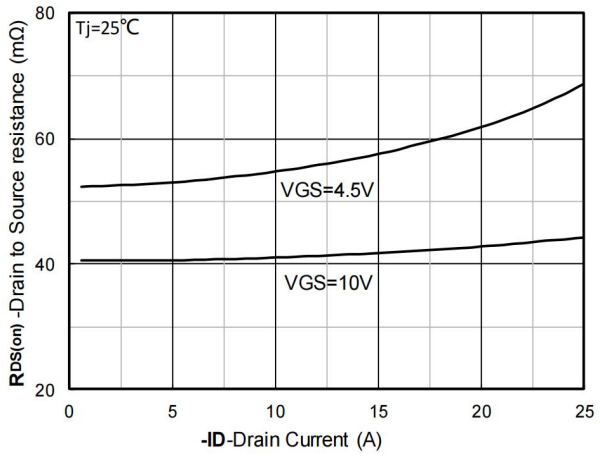


Figure 7. $R_{DS(on)}$ VS Drain Current

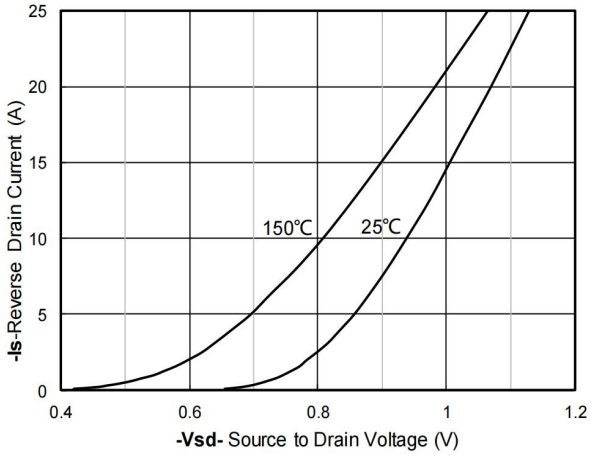


Figure 8. Forward characteristics of reverse diode

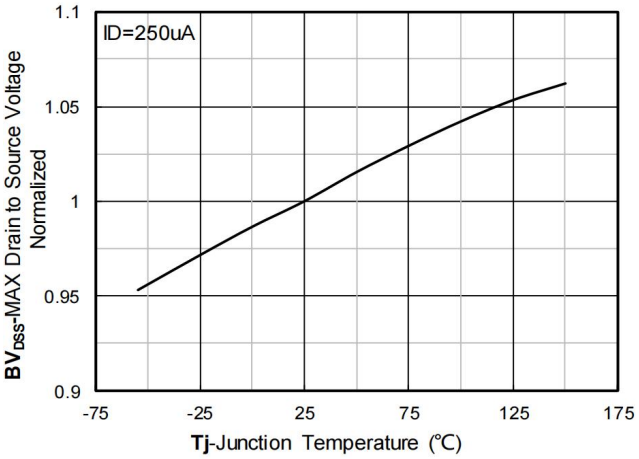


Figure 9. Normalized breakdown voltage

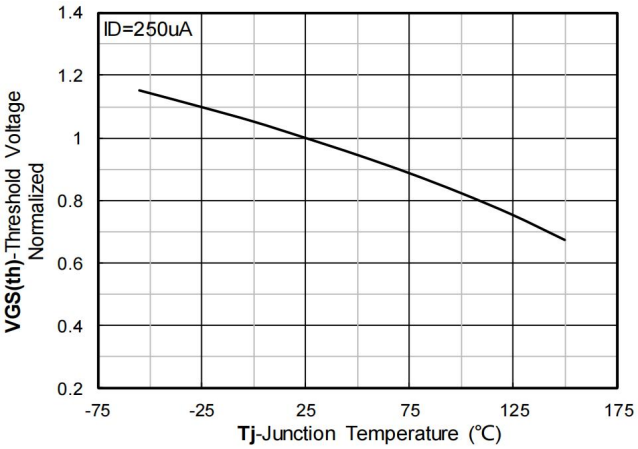


Figure 10. Normalized Threshold voltage

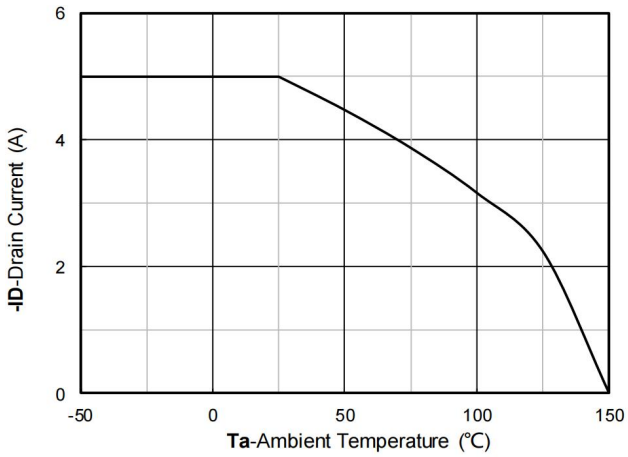


Figure 11. Current dissipation

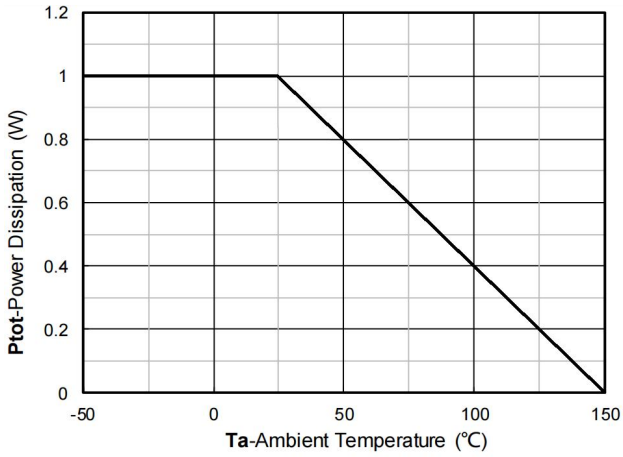


Figure 12. Power dissipation

Typical Characteristics

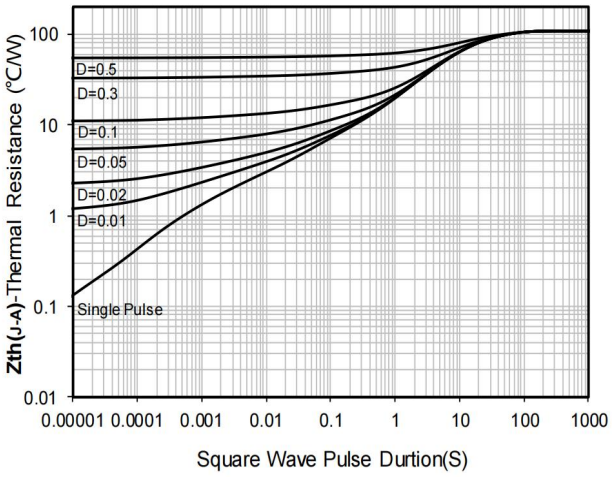


Figure 13. Maximum Transient Thermal Impedance

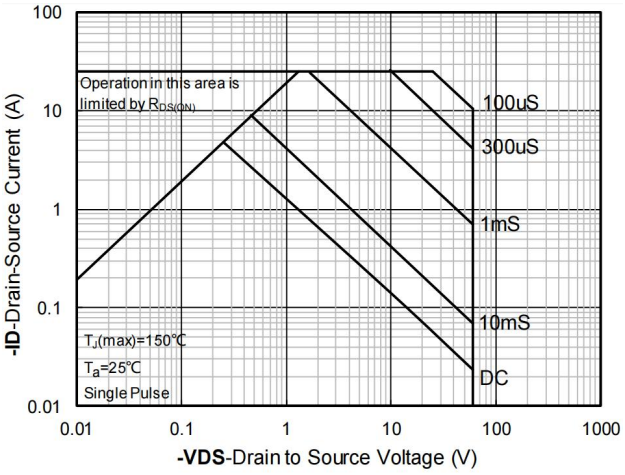
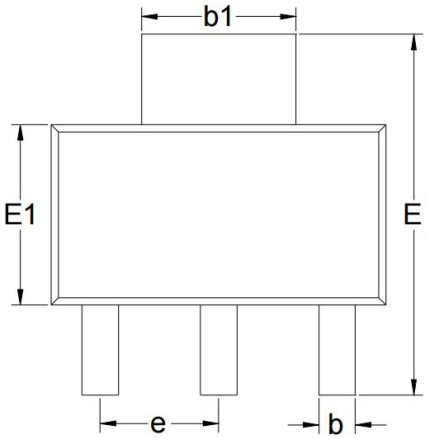
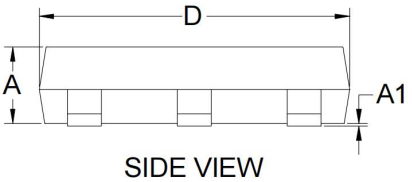


Figure 14. Safe Operation Area

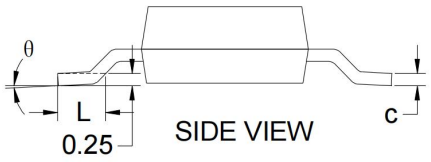
SOT-223 Package Information



TOP VIEW



SIDE VIEW



SIDE VIEW

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.500	1.700	0.059	0.067
A1	0.020	0.100	0.000	0.004
b	0.660	0.840	0.026	0.033
b1	2.900	3.100	0.114	0.122
c	0.230	0.350	0.009	0.014
D	6.300	6.700	0.248	0.264
E	6.700	7.300	0.264	0.287
E1	3.300	3.700	0.130	0.146
e	2.200	2.400	0.087	0.094
L	0.750	1.250	0.030	0.049
θ	0°	10°	0°	10°