

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	I_D
150V	45mΩ@10V	40A

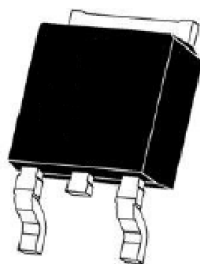
Feature

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Suffix“-Q1”for AEC-Q101

Application

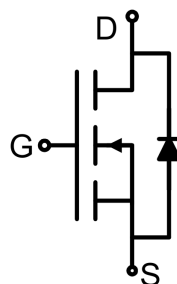
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

Package

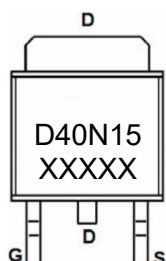


TO-252AB

Circuit diagram



Marking



Absolute maximum ratings (Ta=25°C° unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	150	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	40	A
Pulsed Drain Current	I_{DM}	160	A
Power Dissipation	P_D	140	W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.07	°C/W
Single pulse avalanche energy	E_{AS}	310	mJ
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55 ~ +150	°C

Electrical characteristics (T_A=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D =250μA	150			V
Zero gate voltage drain current	I _{DSS}	V _{DS} =150V,V _{GS} = 0V			1	μA
Gate-body leakage current	I _{GSS}	V _{GS} =±20V, V _{DS} = 0V			±100	nA
Gate threshold voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2.5		4.5	V
Drain-source on-resistance ¹⁾	R _{DS(on)}	V _{GS} =10V, I _D =18A		35	45	mΩ
Forward transconductance ¹⁾	g _{FS}	V _{DS} =15V, I _D =18A	38			S
Dynamic characteristics ²⁾						
Input Capacitance	C _{iss}	V _{DS} =25V,V _{GS} =0V,f =1MHz		4200		pF
Output Capacitance	C _{oss}			203		
Reverse Transfer Capacitance	C _{rss}			96		
Total Gate Charge	Q _g	V _{DS} =30V,V _{GS} =10V,ID =30A		105		nC
Gate-Source Charge	Q _{gs}			21		
Gate-Drain Charge	Q _{gd}			31.5		
Turn-on delay time	t _{d(on)}	V _{DD} =30V,V _{GS} =10V, I _D =2A R _L =15Ω,R _{GEN} =2.5Ω		17.8		nS
Turn-on rise time	t _r			11.8		
Turn-off delay time	t _{d(off)}			56		
Turn-off fall time	t _f			14.6		
Source-Drain Diode characteristics						
Diode Forward Current ¹⁾	I _S				40	A
Diode Forward voltage	V _{DS}	V _{GS} =0V, I _S =18A			1.2	V
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =18A		70		nS
Reverse Recovery Charge	Q _{rr}	di/dt = 100A/μs ¹⁾		230		nC

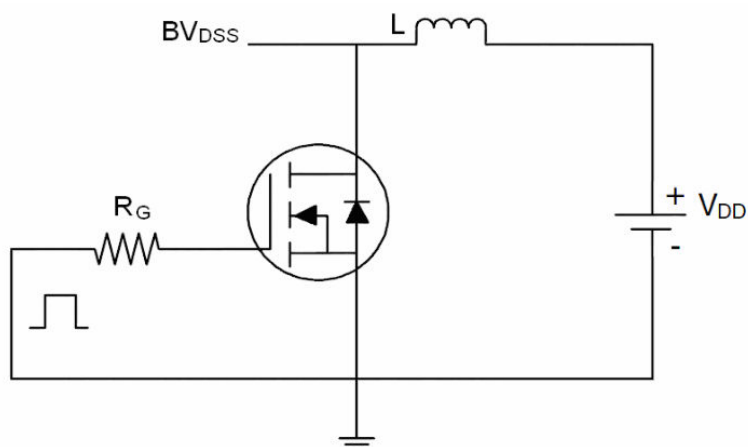
Notes:

1) Pulse Test: Pulse Width < 300μs, Duty Cycle ≤2%.

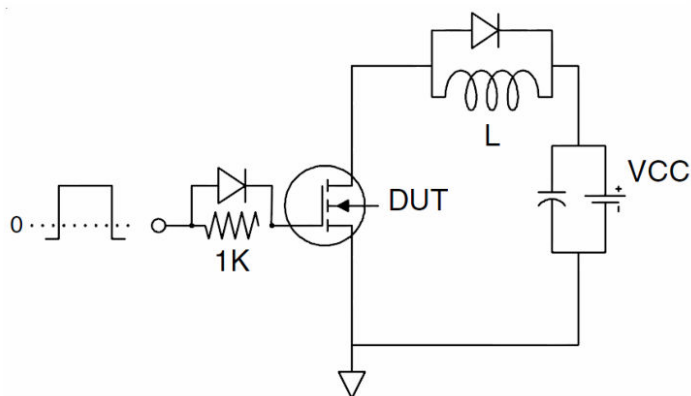
2) Guaranteed by design, not subject to production testing.

Test Circuit

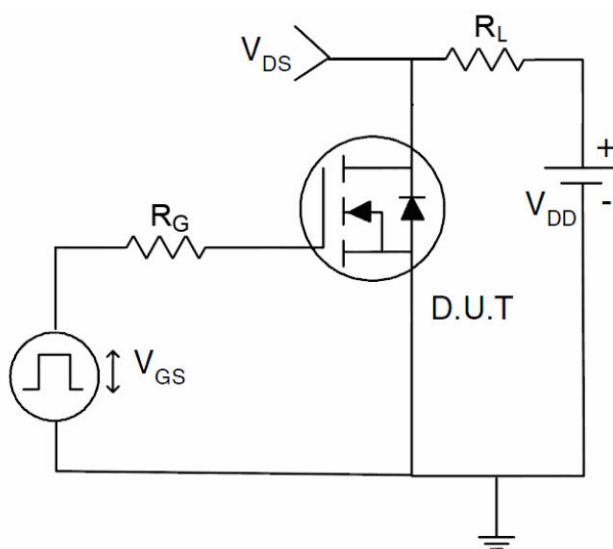
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit



Typical Characteristics

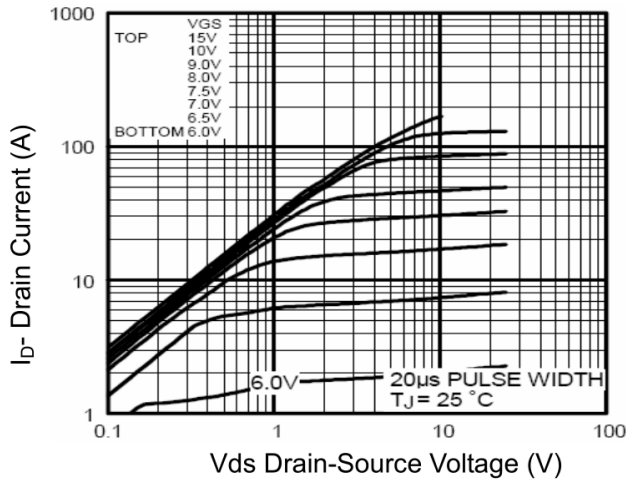


Figure 1 Output Characteristics

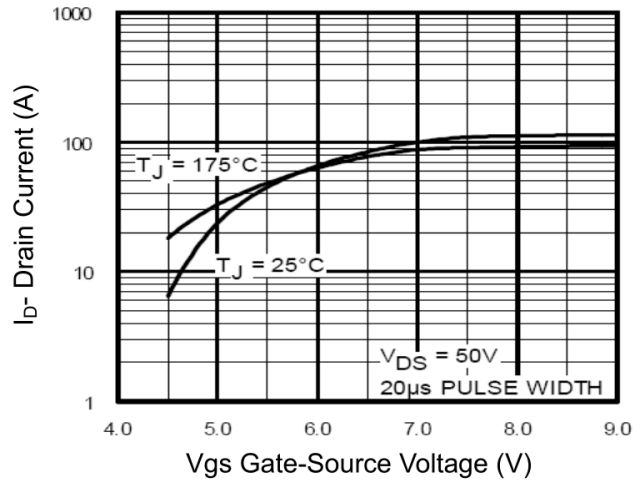


Figure 2 Transfer Characteristics

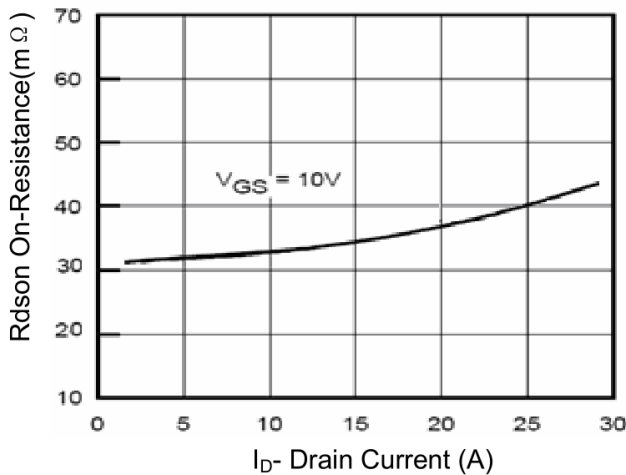


Figure 3 $R_{DS(on)}$ - Drain Current

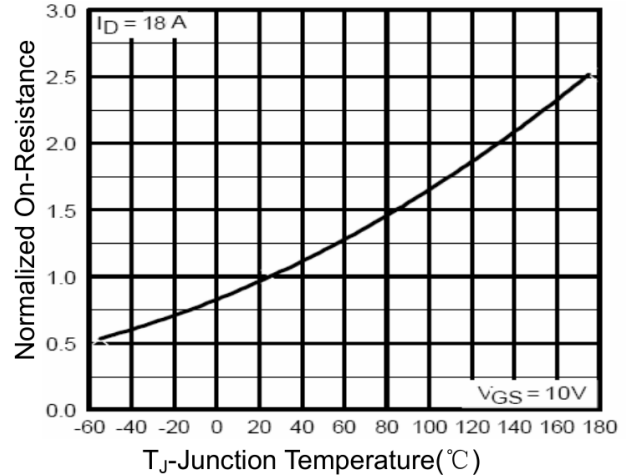


Figure 4 $R_{DS(on)}$ -Junction Temperature

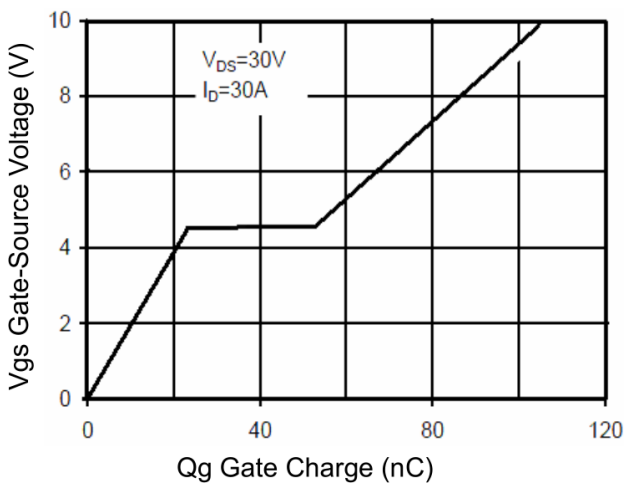


Figure 5 Gate Charge

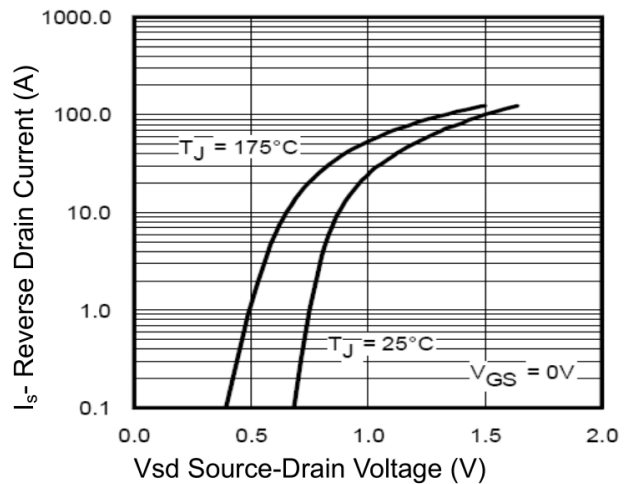


Figure 6 Source- Drain Diode Forward

Typical Characteristics

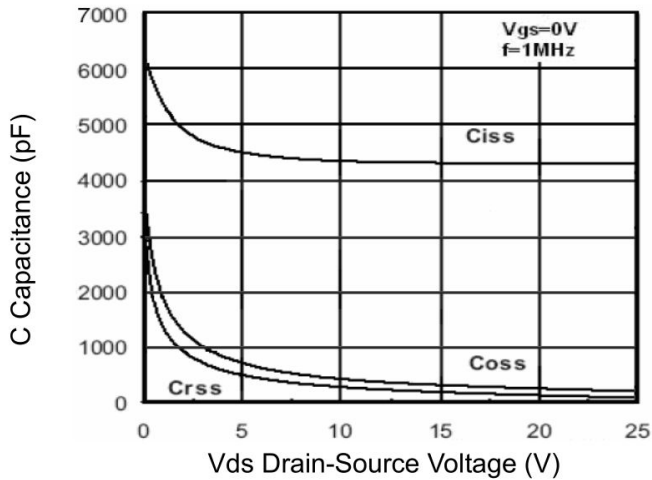


Figure 7 Capacitance vs Vds

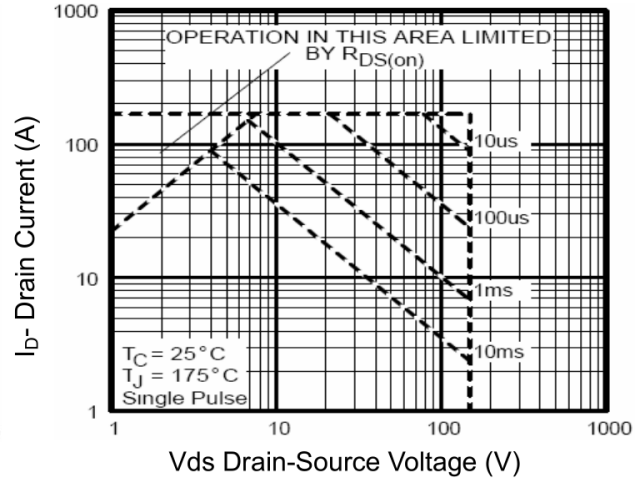


Figure 8 Safe Operation Area

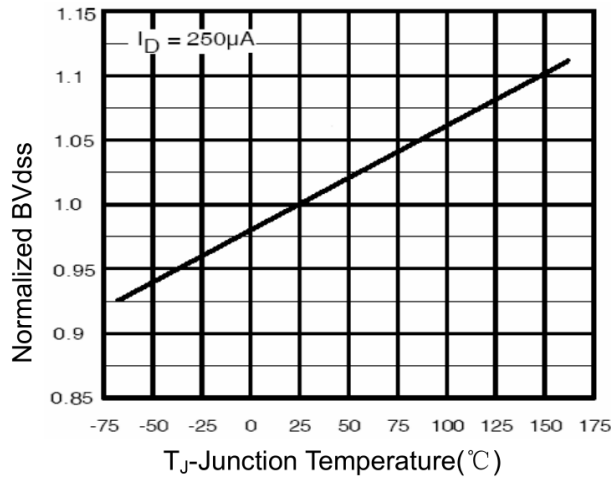


Figure 9 BV_{DSS} vs Junction Temperature

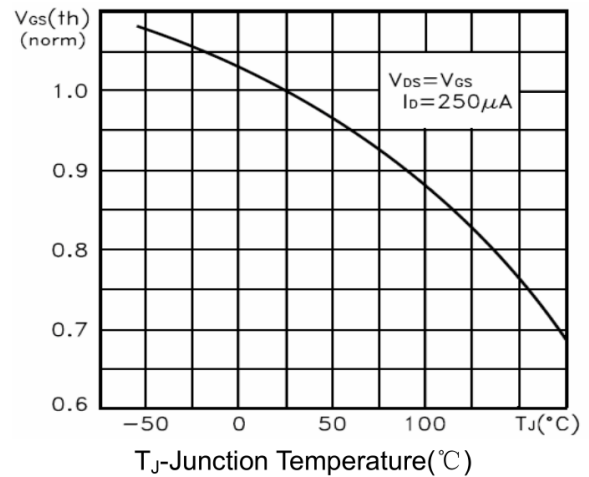


Figure 10 V_{GS(th)} vs Junction Temperature

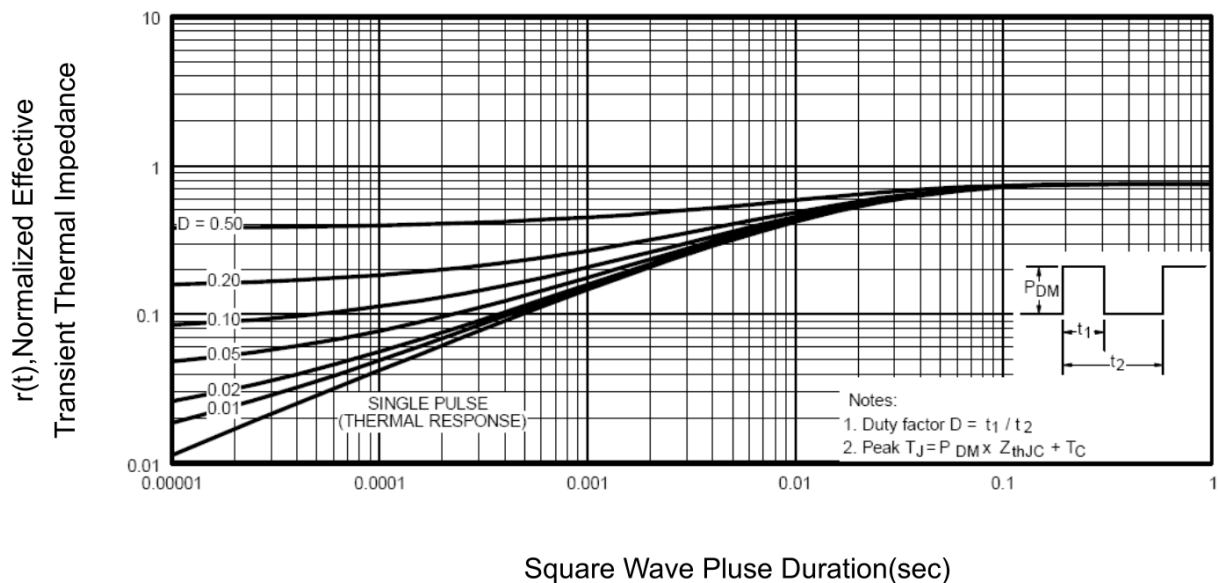
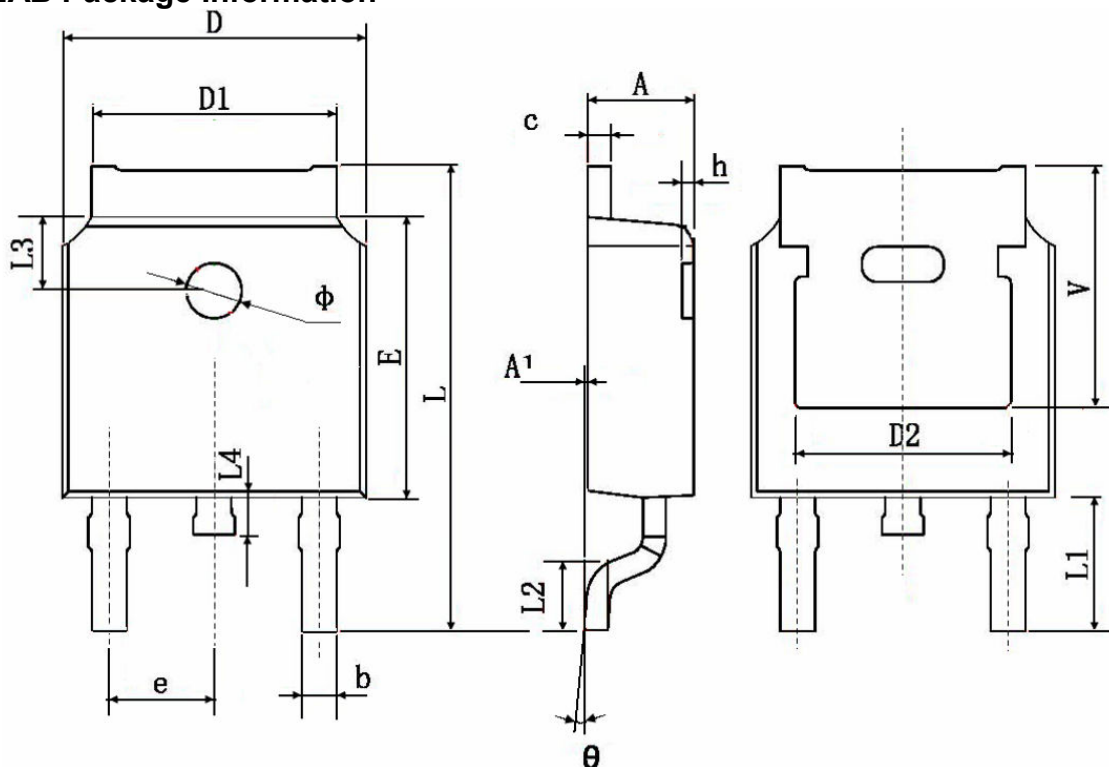


Figure 11 Normalized Maximum Transient Thermal Impedance

TO-252AB Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	