

### Product Summary

$V_{(BR)DSS}$	$R_{DS(on)MAX}$	$I_D$
100V	110mΩ@10V	15A
	120mΩ@4.5V	

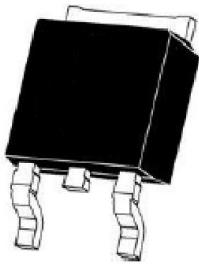
### Feature

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation
- Suffix “-Q1” for AEC-Q101

### Application

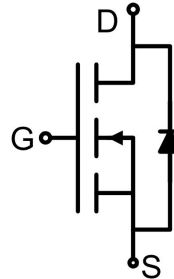
- DC-DC Converters
- Power management functions

### Package

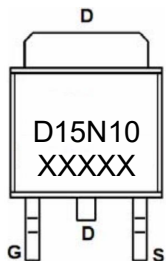


TO-252AB

### Circuit diagram



### Marking



### Absolute maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	15	A
Pulsed Drain Current	$I_{DM}$	60	A
Power Dissipation	$P_D$	34	W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	4.4	$^{\circ}C/W$
Single pulse avalanche energy	$E_{AS}$	9	mJ
Junction Temperature	$T_J$	150	$^{\circ}C$
Storage Temperature	$T_{STG}$	-55 ~ +150	$^{\circ}C$

### Electrical characteristics (TA=25 °C, unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100			V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 100V, V_{GS} = 0V$			1	$\mu A$
Gate-body leakage current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			$\pm 100$	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.1		3.0	V
Drain-source on-resistance <sup>1)</sup>	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 8A$			110	m $\Omega$
		$V_{GS} = 4.5V, I_D = 8A$			120	
<b>Dynamic characteristics<sup>2)</sup></b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 50V, V_{GS} = 0V, f = 1MHz$		800		pF
Output Capacitance	$C_{oss}$			39		
Reverse Transfer Capacitance	$C_{rss}$			32		
Total Gate Charge	$Q_g$	$V_{DS} = 50V, V_{GS} = 10V, I_D = 10A$		16		nC
Gate-Source Charge	$Q_{gs}$			2.5		
Gate-Drain Charge	$Q_{gd}$			2.6		
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 50V, V_{GS} = 10V, R_L = 6.4\Omega, R_{GEN} = 3\Omega$		5		nS
Turn-on rise time	$t_r$			40		
Turn-off delay time	$t_{d(off)}$			20		
Turn-off fall time	$t_f$			7		
<b>Source-Drain Diode characteristics</b>						
Diode Forward Current <sup>1)</sup>	$I_S$				15	A
Diode Forward voltage	$V_{DS}$	$V_{GS} = 0V, I_S = 15A$			1.2	V

Notes:

- 1) Pulse Test: Pulse Width < 300 $\mu s$ , Duty Cycle  $\leq 2\%$ .
- 2) Guaranteed by design, not subject to production testing.

## Typical Characteristics

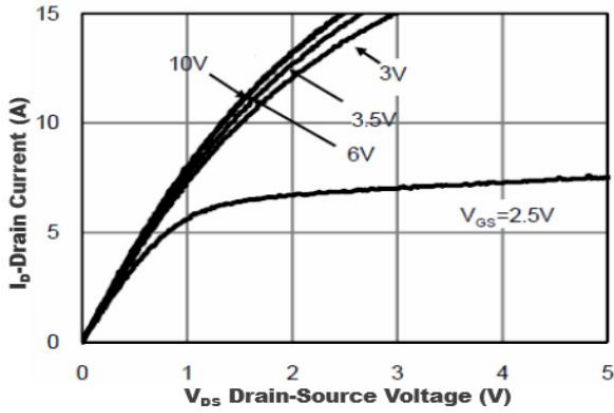


Figure1. Output Characteristics

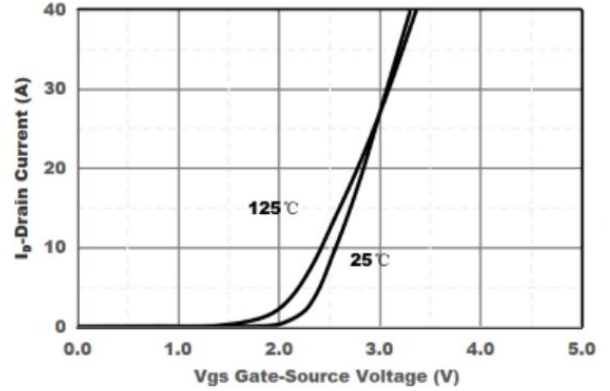


Figure2. Transfer Characteristics

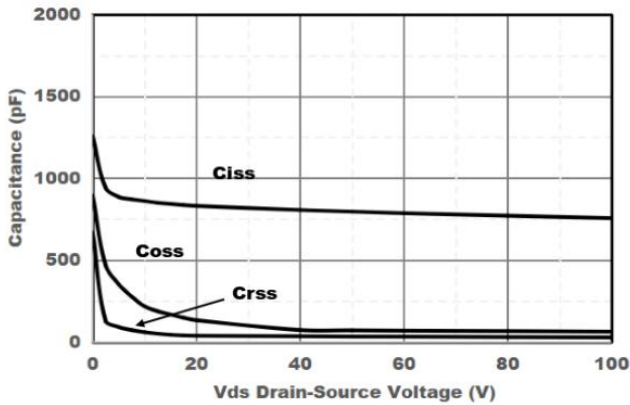


Figure3. Capacitance Characteristics

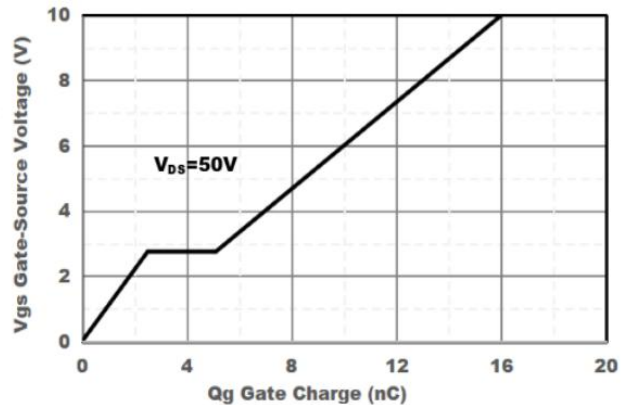


Figure4. Gate Charge

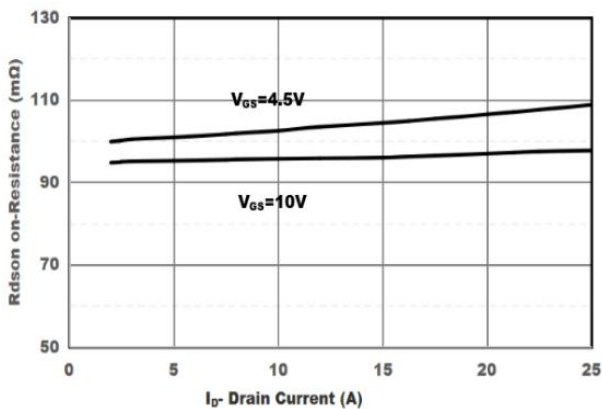


Figure5. Drain-Source on Resistance

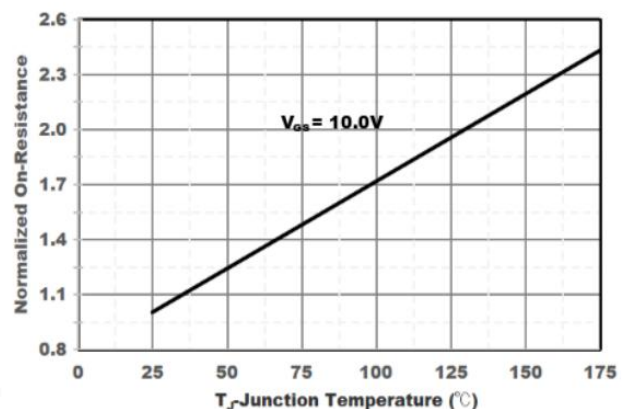
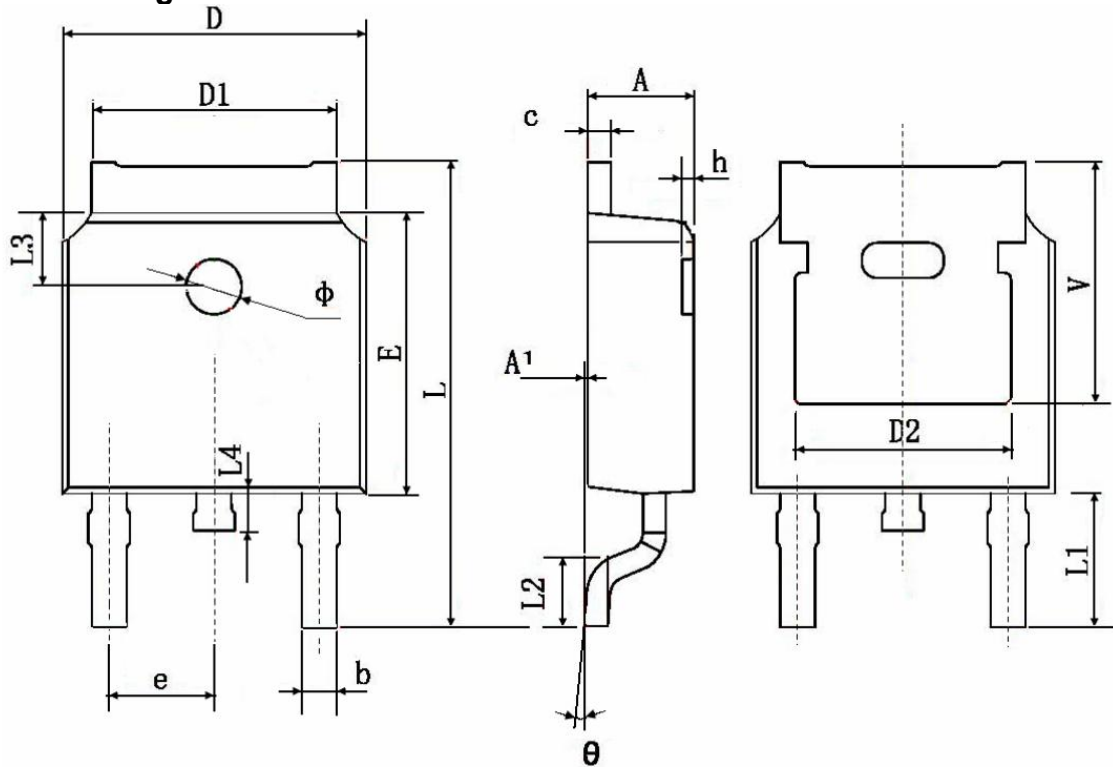


Figure6. Drain-Source on Resistance

### TO-252AB Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	